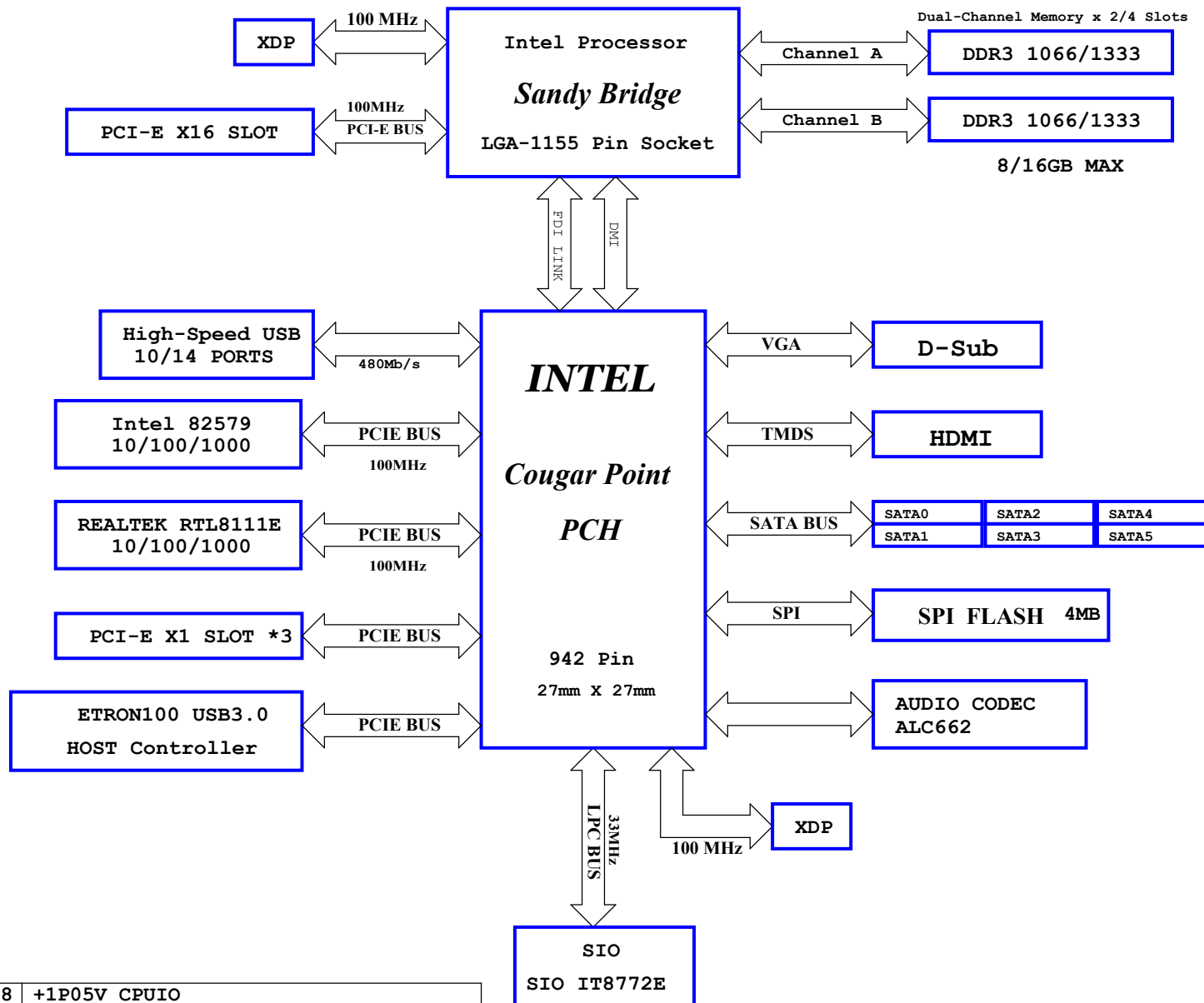


IPISB-VR

Revision: 1.01

PAGE	TITLE
01	BLOCK DIAGRAM
02	CLOCKS DISTRIBUTION
03	SIGNAL & RESET MAP
04	CHANGE HISTORY
05	POWER FLOW
06	POWER DISTRIBUTION
07	POWER SEQUENCE
08~13	INTEL CPU SOCKET1155(1~6)
14	PLTRST CPU#
15~16	DDR3 CHANNEL A&B
17	DDR3 TERMINATION A&B
18	XXXX
19~27	INTEL PCH(1~9)
28	PCH DPWROK & SUS ACK#
29	INTEGRATED VGA PORT
30	XXXX
31	HDMI CONN
32	PCI EXPRESS X16 SLOT
33	PCI EXPRESS X1 SLOT
34	PCI EXPRESS X1 SLOT
35	RSMRST
36	SUPER I/O IT8772E
37~38	INTEL 82579 LAN
39	REALTEK RTL8111E CONTROLLER
40	RJ45+USB CONNECTOR
41	REAR USB-4 PORTS
42	SIS USB 3.0 CONTROLLER
43	USB 3.0 CONNECTOR
44	USB PORT CONTROL
45	USB POWER
46	USB HEADER - 1
47	USB HEADER - 2
48	REALTEK ALC662 CODEC
49	FRONT AUDIO CONNECTOR
50	REAR AUDIO CONNECTOR
51	LPC DEBUG
52	SATA CONNECTOR
53	SM BUS & SPI ROM
54	FRONT PANEL CIRCUIT
55	RTC / CMOS / SPKR/ SCREW
56	CPU XDP DEBUG CONNECTOR
57	PCH XDP DEBUG CONNECTOR
58	FAN CIRCUIT
59	KB & MS
60	ATX POWER 24P CONNECTOR
61	+3VA & +3VSB & +5VSB
62	+1P5V DUAL & +1P8V SFR
63	+VTT DDR & +1P5V DUAL EN
64	+1P05V PCH & +0P925V SA
65	+5V DUAL & +5V DUAL USB B/F
66	XXXX
67	+1P8V&+3P3V LAN & +3P3V ME

68	+1P05V CPUIO
69	+1P05V ME&+1P05V CPUIO CAP
70	VCORE CONTROLLER
71	VCORE DRIVER 1-2
72	VCORE DRIVER 2-2
73	VCORE CAP
74	+V AXG DRIVER
75	OV CONTROLLER
76	EMI CAP



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PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : **BLOCK DIAGRAM**

Pegatron Corp. Engineer: **Tim Chen**

Size A3 Project Name **IPISB-VR** Rev 1.01

Date: Wednesday, December 08, 2010 Sheet 1 of 76

Schematics Change History

[illegible]

CAD Note:

Property: BOM

I = Installed Part.

NI = Not Installed Part.

PROTO = PROTO Phase Only.

VP = Virtual Part.

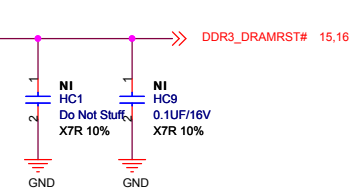


67-00000-01 PEGATRON DT-MB RESTRICTED SECRET

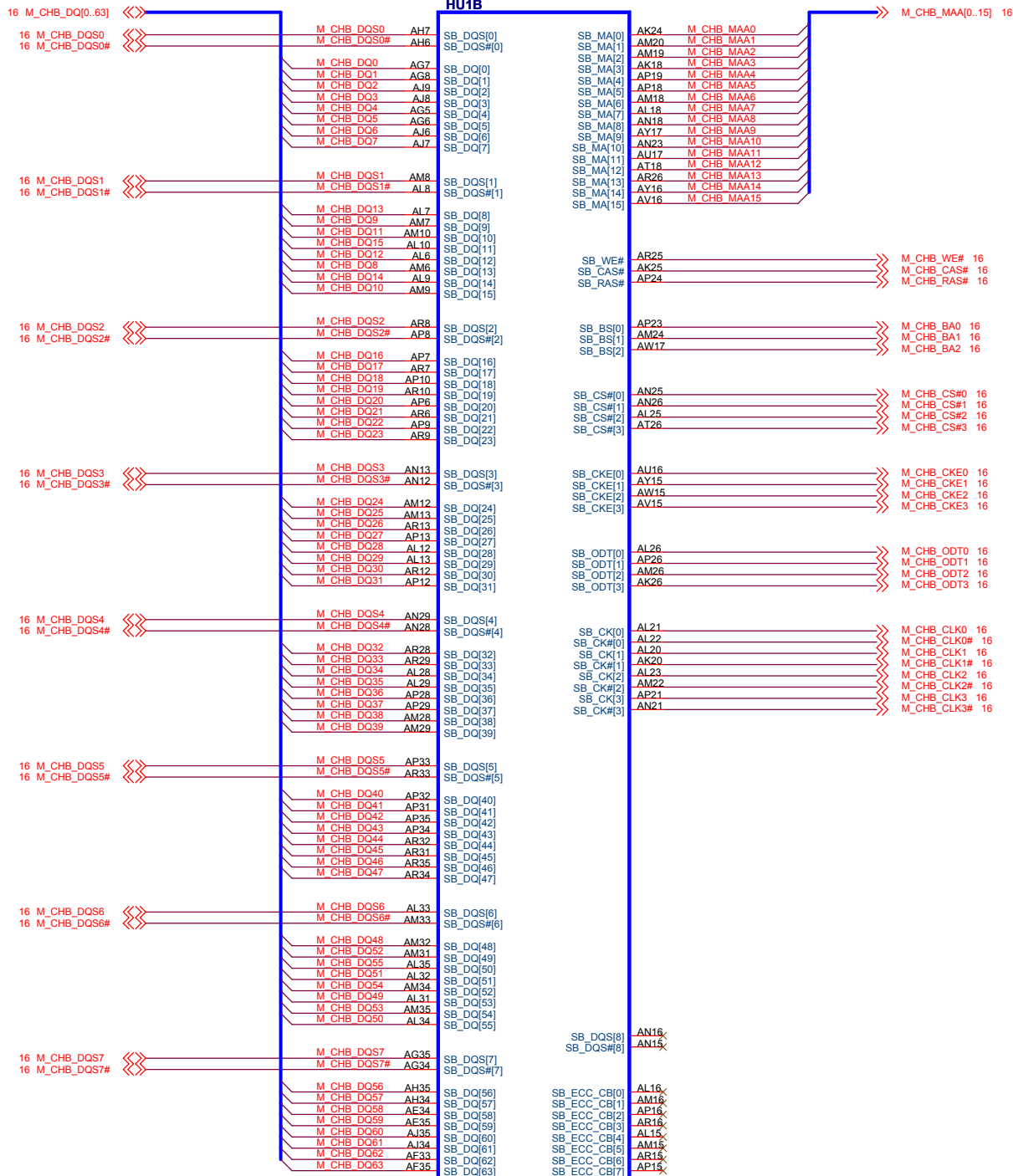
PEGATRON Title : **POWER FLOW**

Pegatron Corp. Engineer: **Tim Chen**

Size A3	Project Name IPISB-VR	Rev 1.01
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Date: Wednesday, December 08, 2010 Sheet 8 of 76



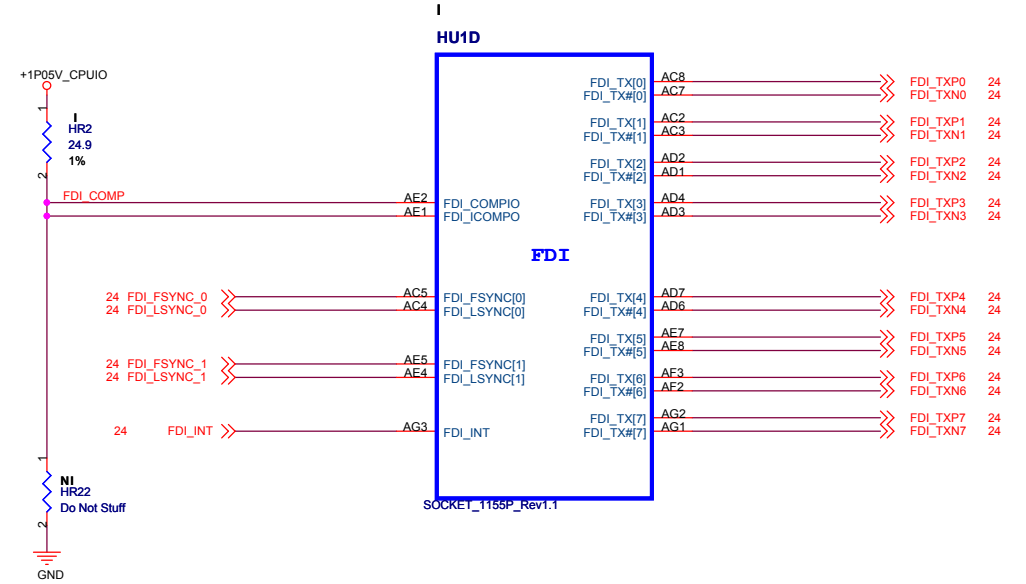
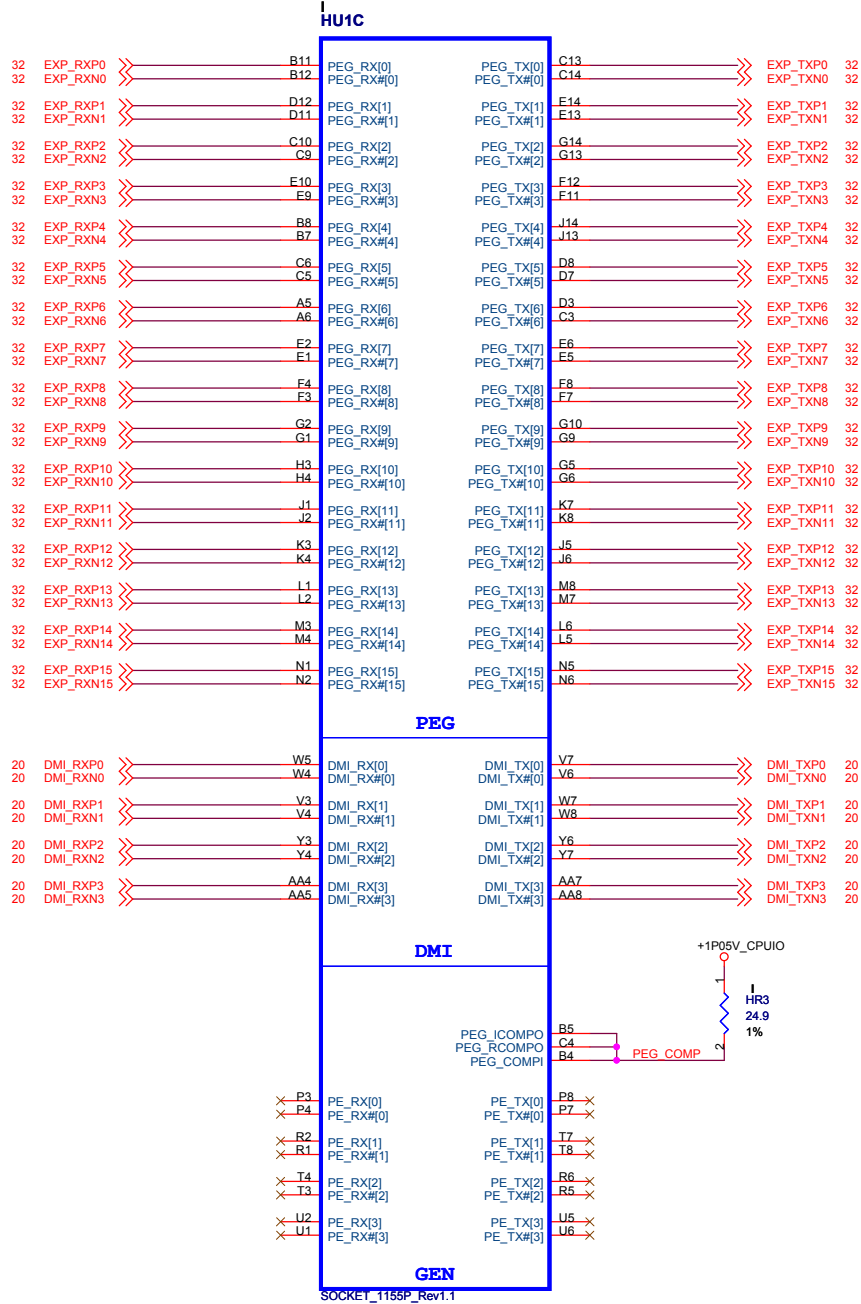
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : DDR3 B 2-6

Pegatron Corp. Engineer: Tim Chen

Size A3 Project Name IPISB-VR Rev 1.01

Date: Wednesday, December 08, 2010 Sheet 9 of 76



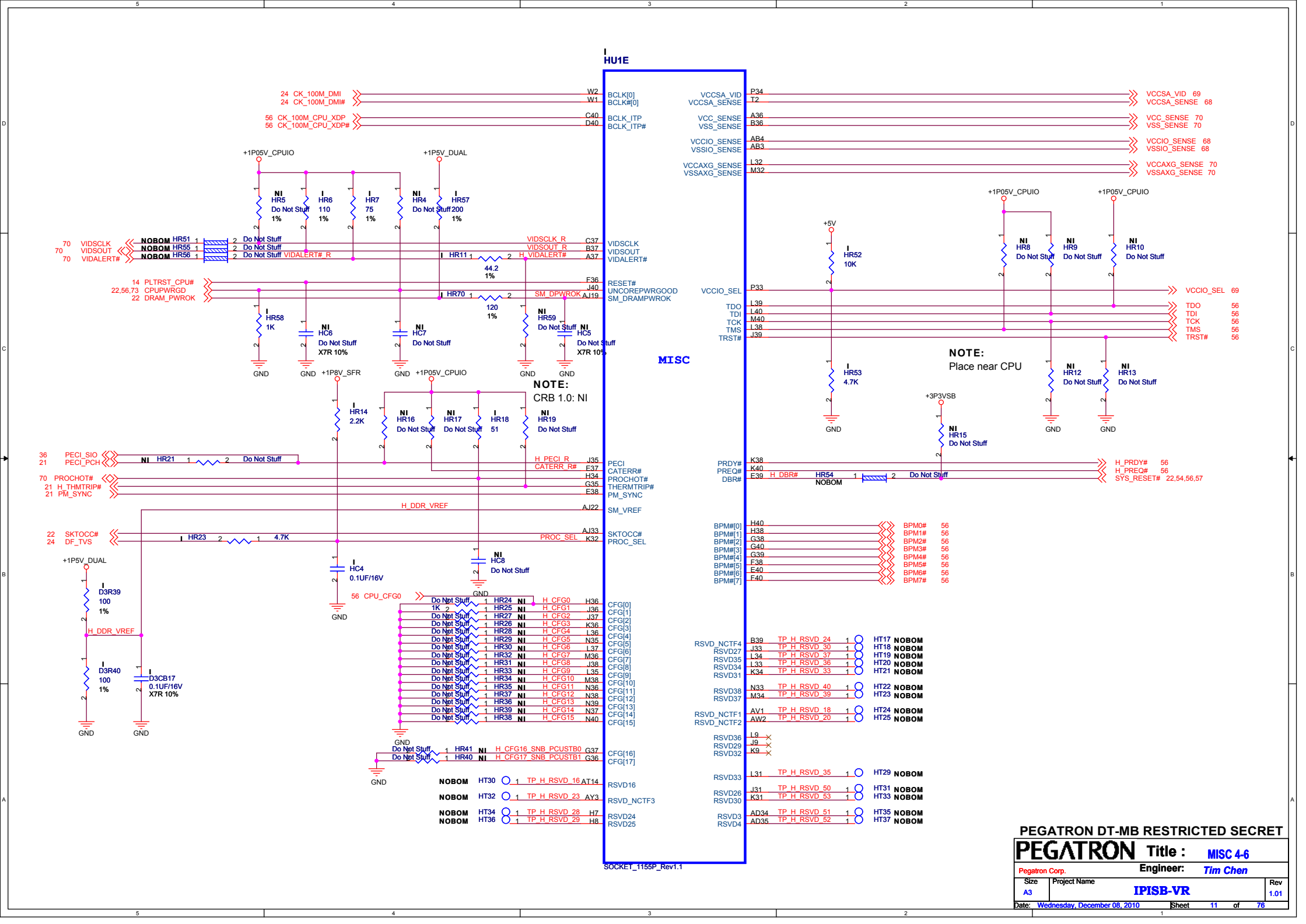
PEGATRON DT-MB RESTRICTED SECRET

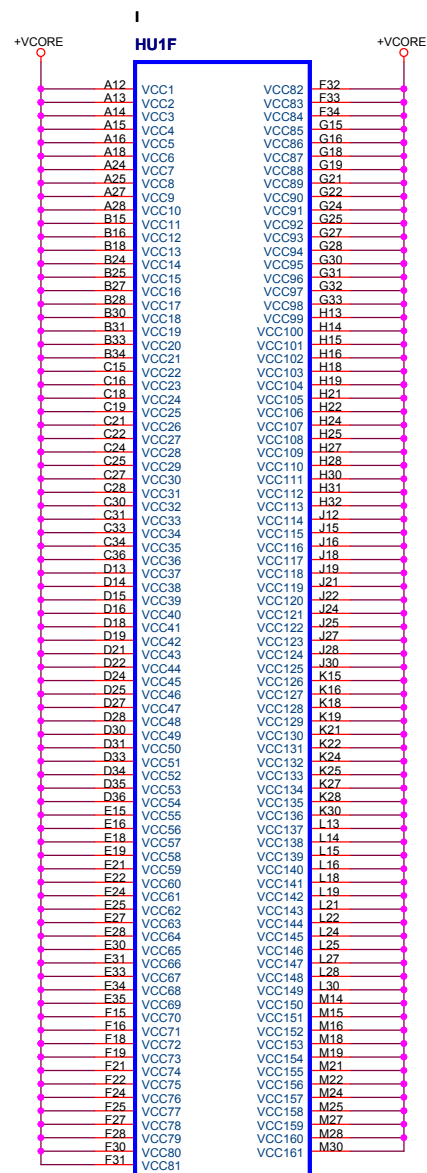
PEGATRON Title : **PCIE/DMI/FDI 3-6**

Pegatron Corp. Engineer: **Tim Chen**

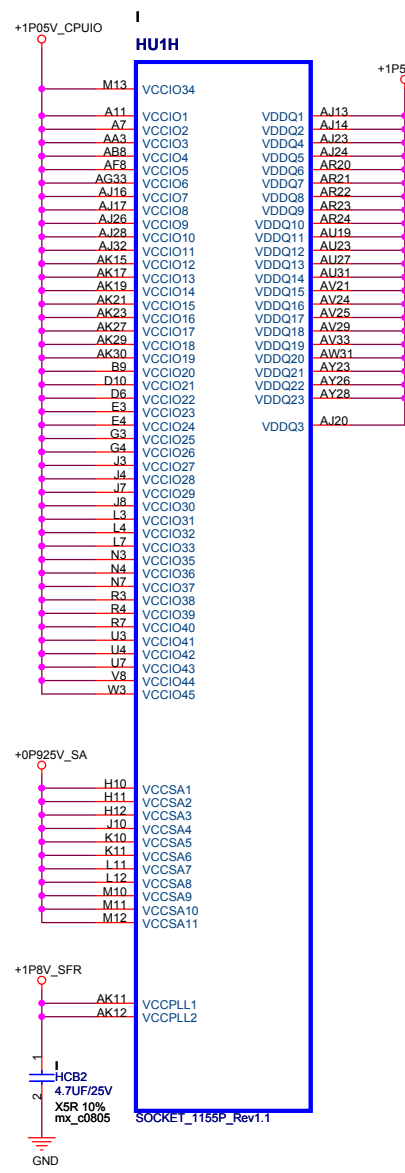
Size	Project Name	Rev
A3	IPISB-VR	1.01

Date: Wednesday, December 08, 2010 Sheet 10 of 76



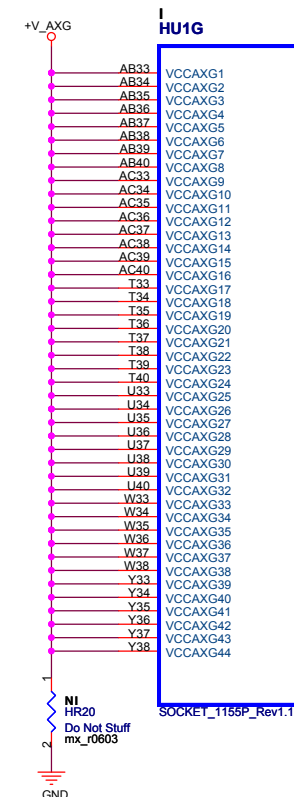
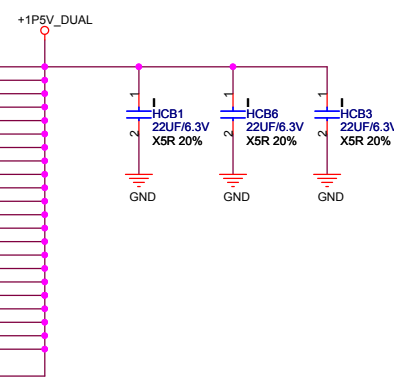


SOCKET_1155P_Rev1.1



+0P925V_SA

+1P8V_SFR



SOCKET_1155P_Rev1.1

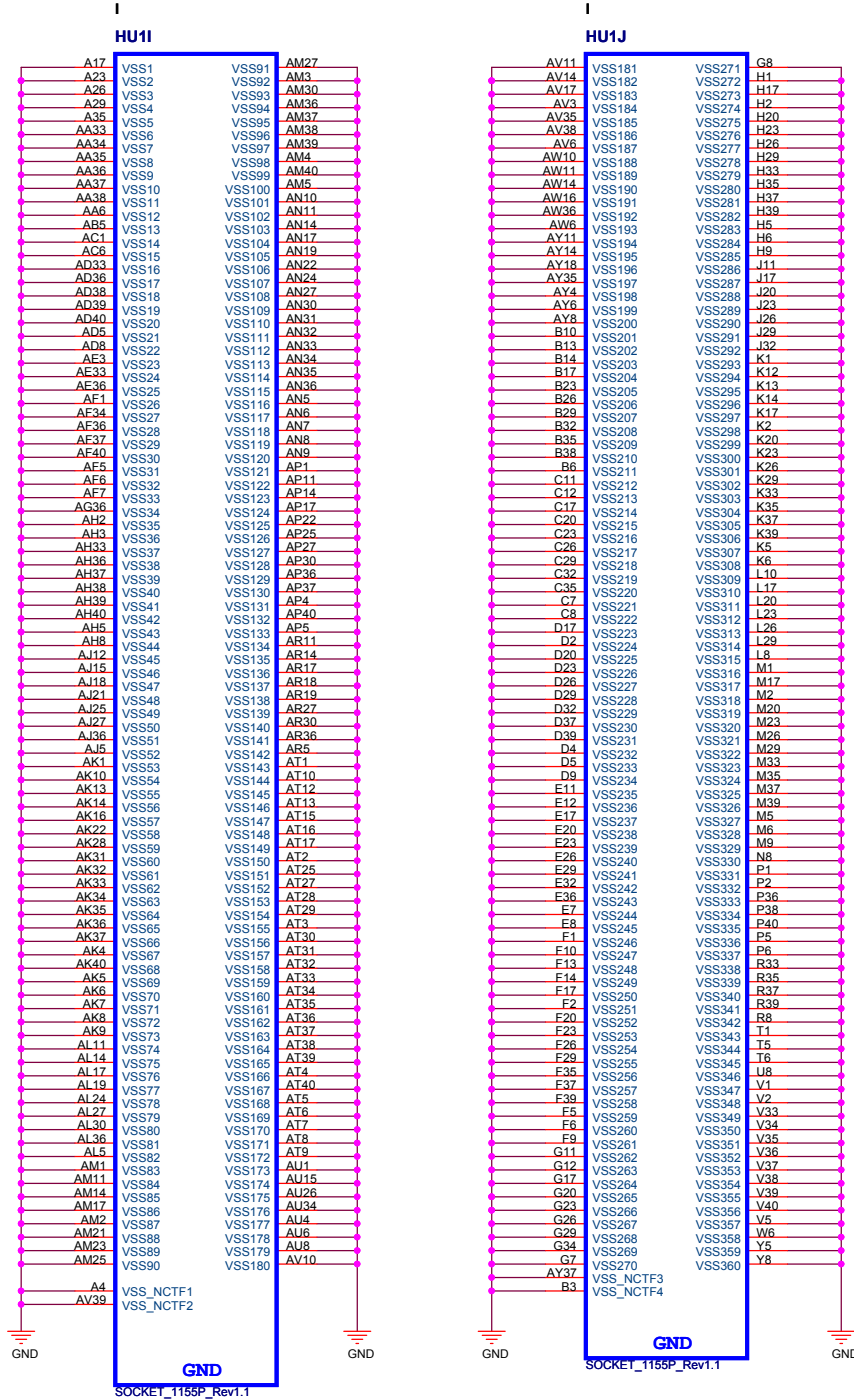
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : VCC 5 - 6

Pegatron Corp. Engineer: Tim Chen

Size	Project Name	Rev
A3	IPISB-VR	1.01

Date: Wednesday, December 08, 2010 Sheet 12 of 76

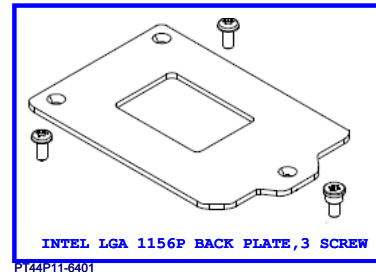


NOBOM HT38 1 TP H NCTF 1

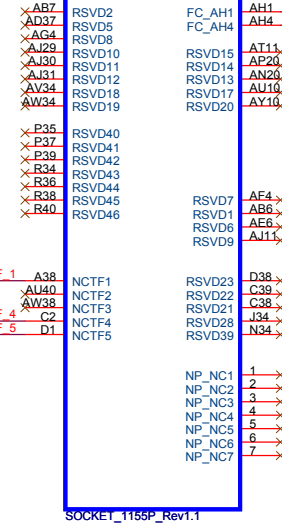
NOBOM HT41 1 TP H NCTF 4

NOBOM HT42 1 TP H NCTF 5

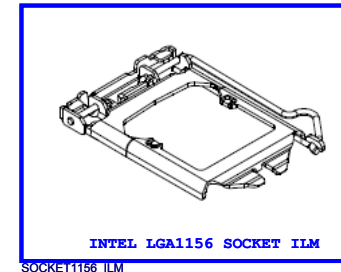
BACKPLATE1



HU1K



ILM1



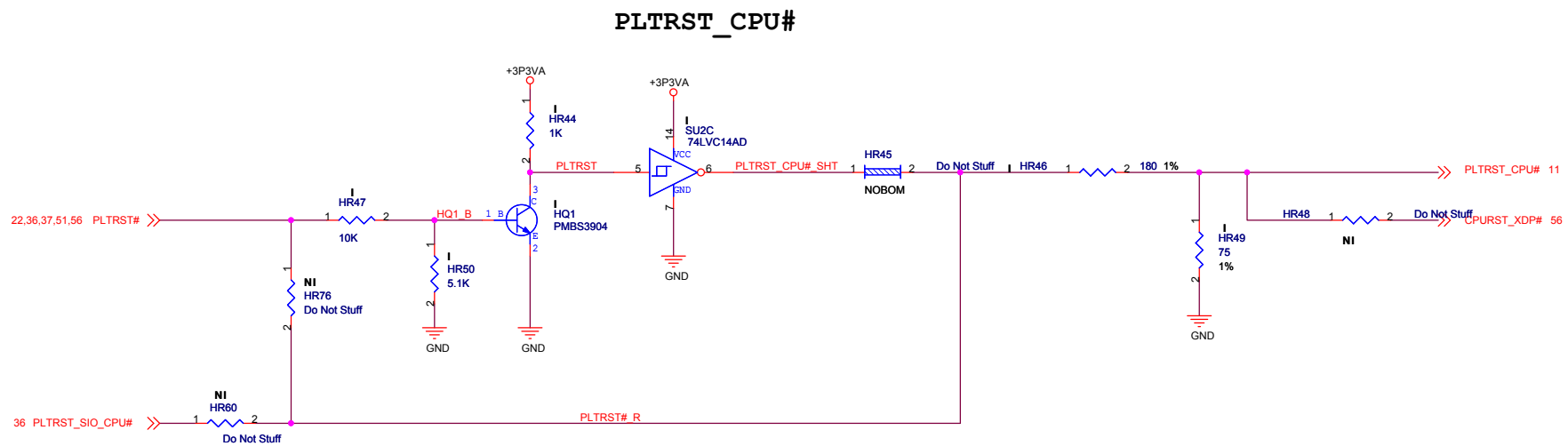
PEGATRON DT-MB RESTRICTED SECRET

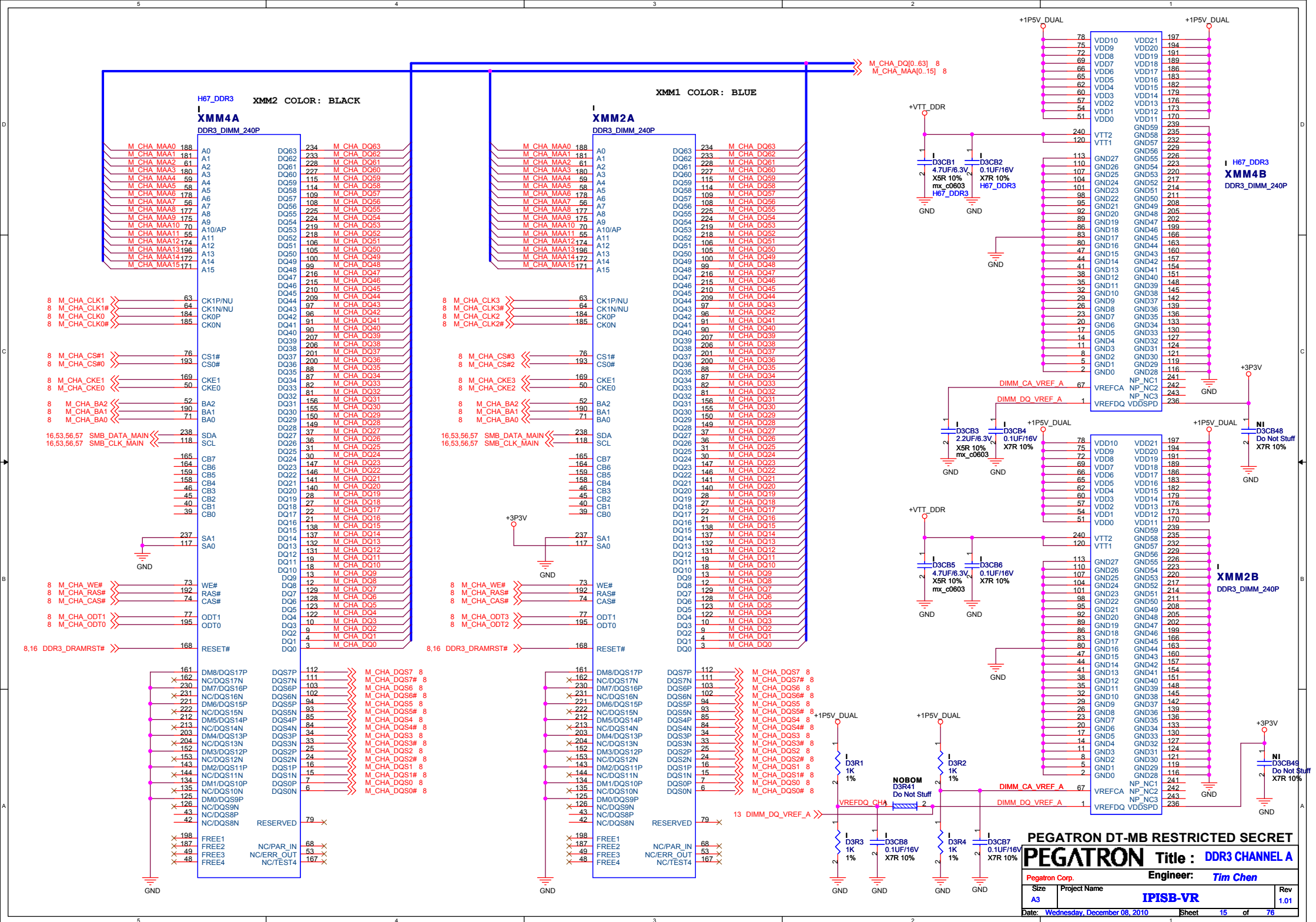
PEGATRON Title : VSS 6 - 6

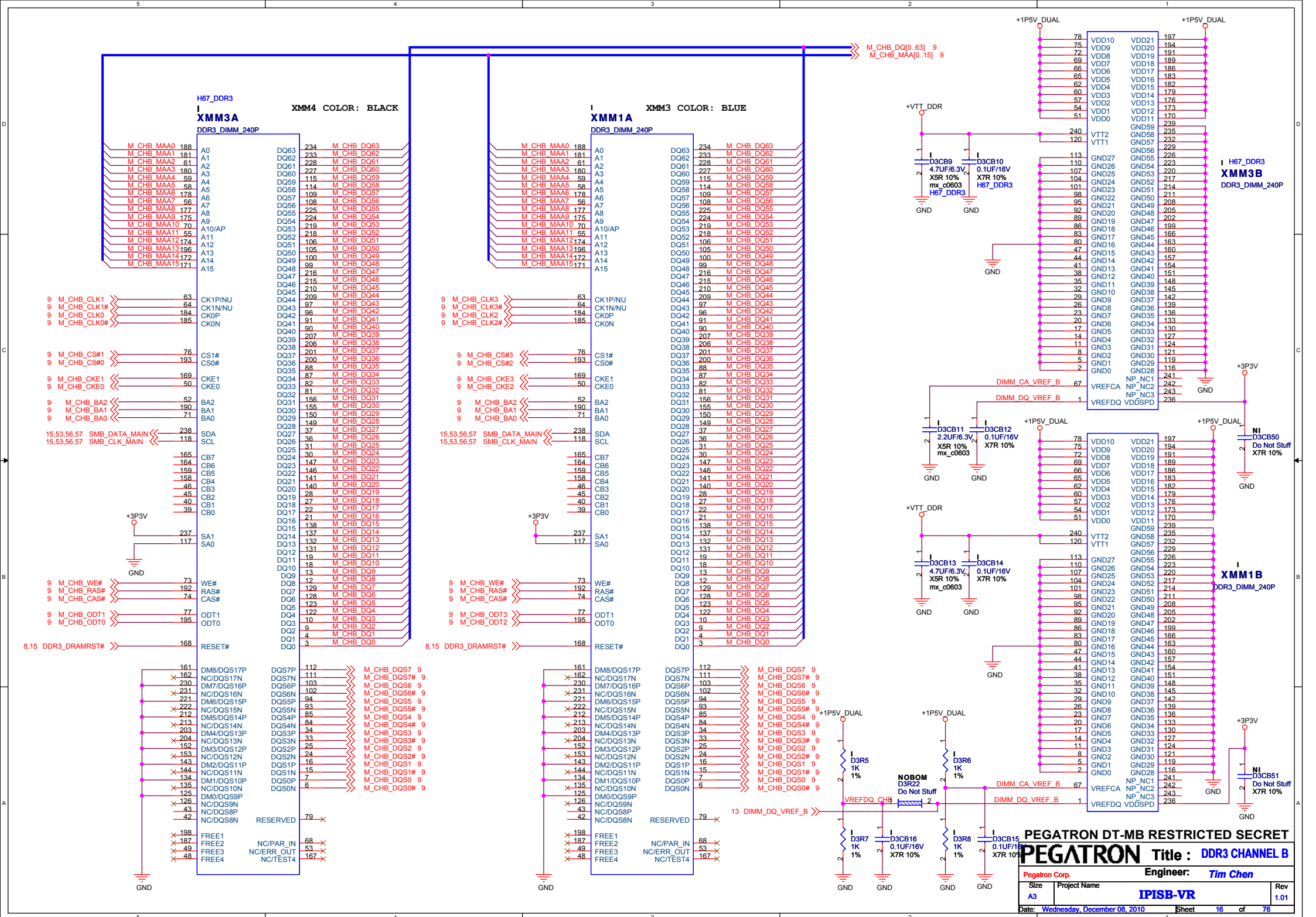
Pegatron Corp. Engineer: Tim Chen

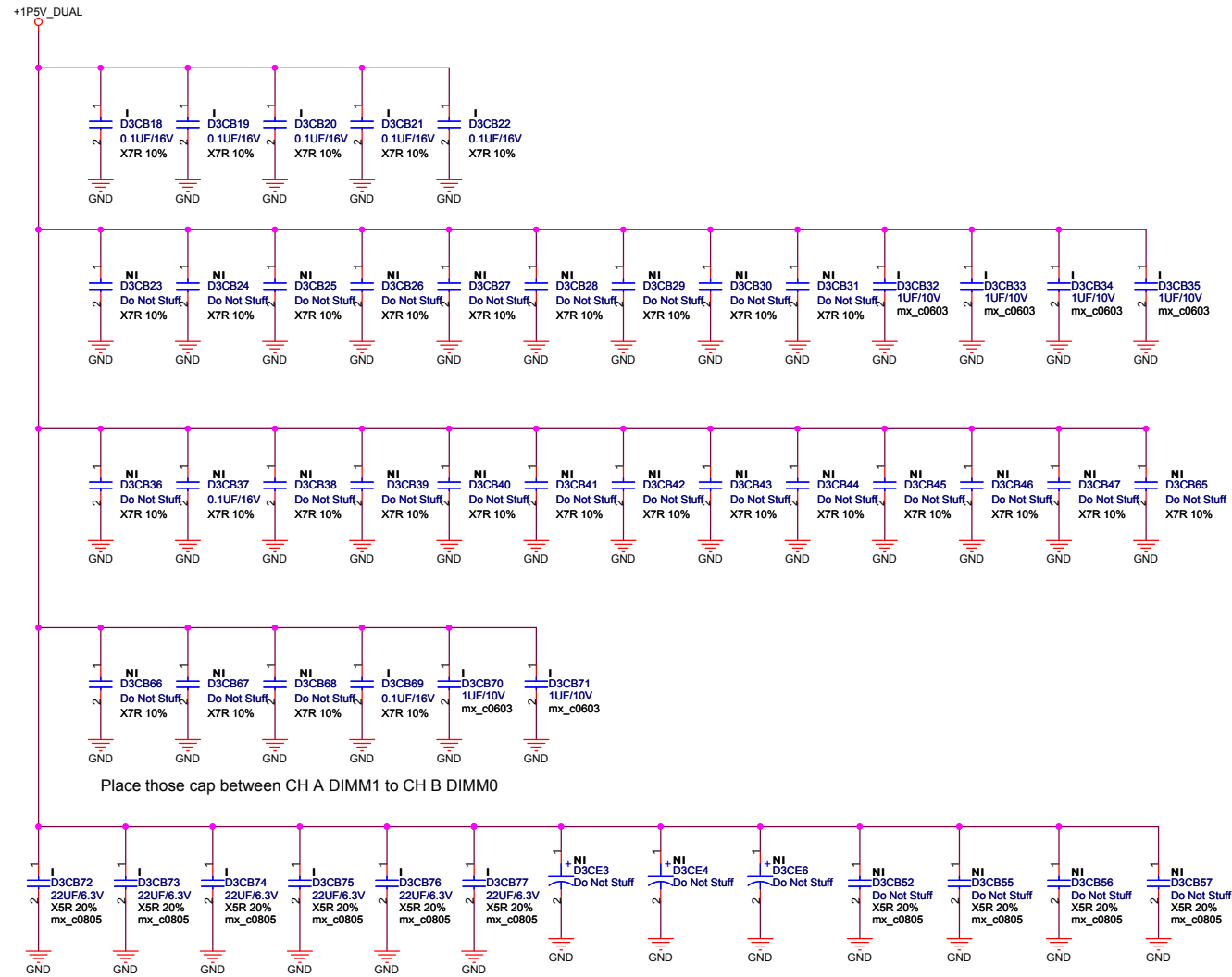
Size A3 Project Name IPISB-VR Rev 1.01

Date: Wednesday, December 08, 2010 Sheet 13 of 76









PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : DDR3 TERMINATION A&B

Pegatron Corp. Engineer: Tim Chen

Size A3	Project Name IPISB-VR	Rev 1.01
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Date: Wednesday, December 08, 2010 Sheet 17 of 76



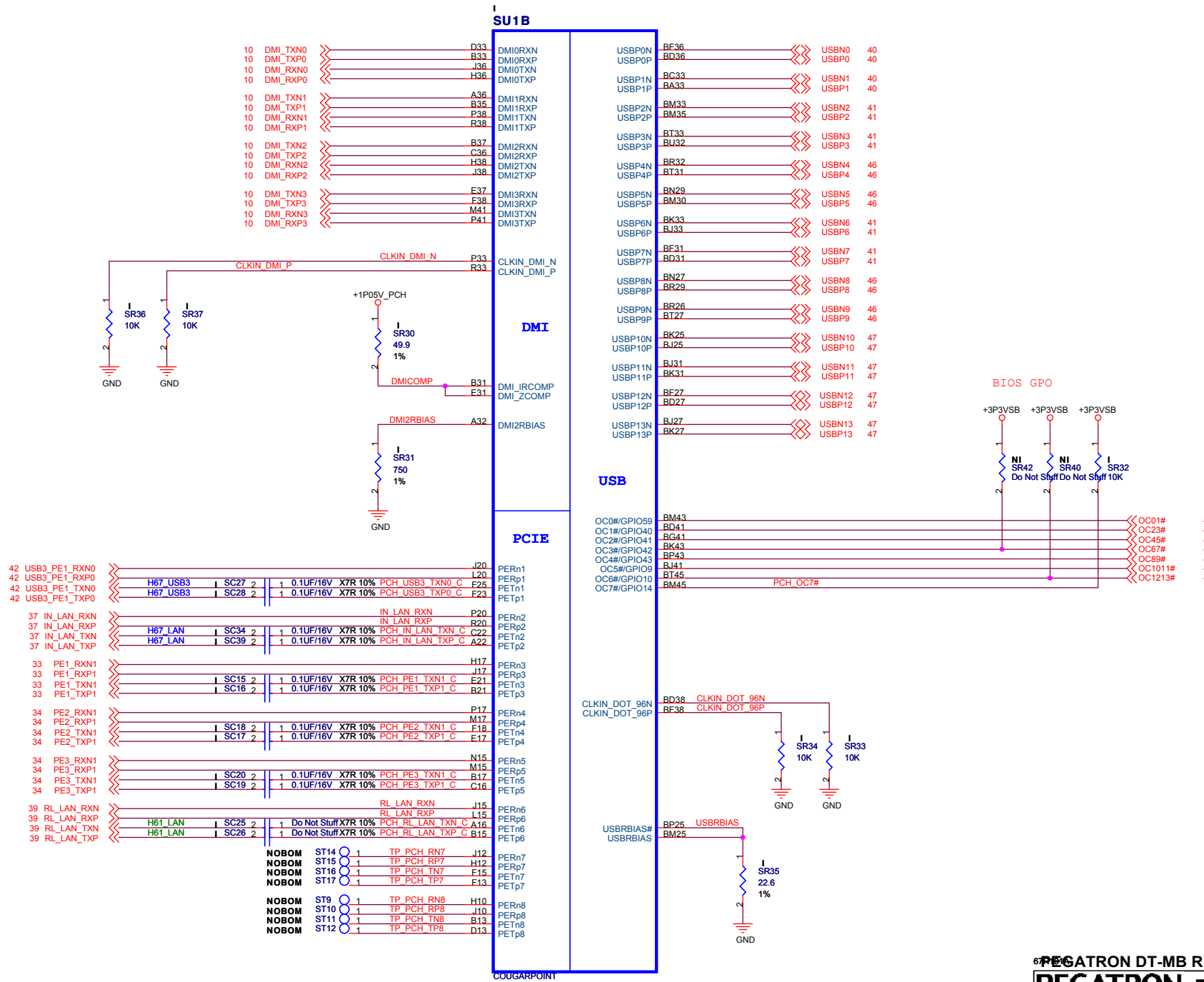
PEGATRON DT-MB RESTRICTED SECRET

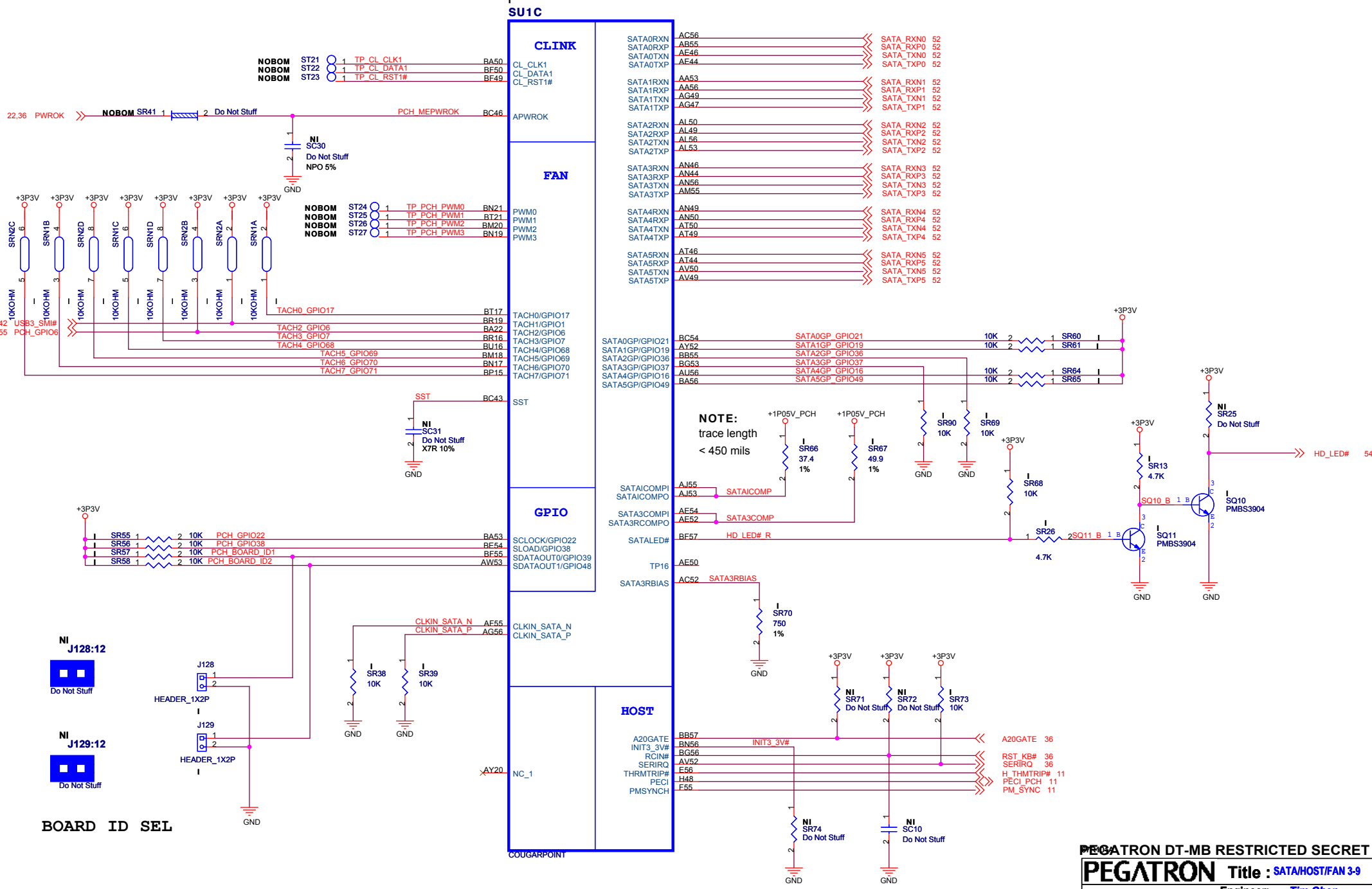
PEGATRON Title : **XXXX**

Pegatron Corp. Engineer: **Tim Chen**

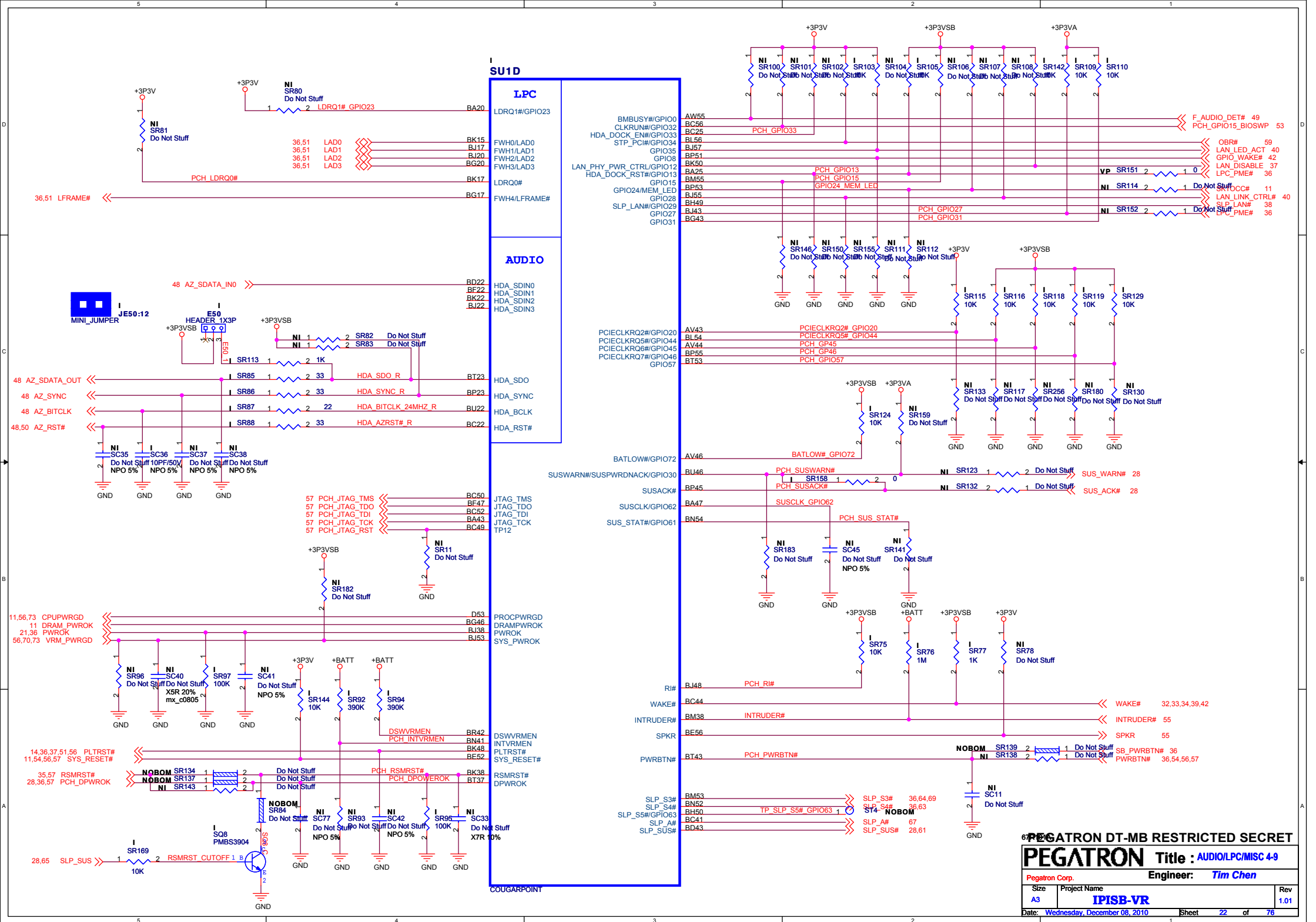
Size A3	Project Name IPISB-VR	Rev 1.01
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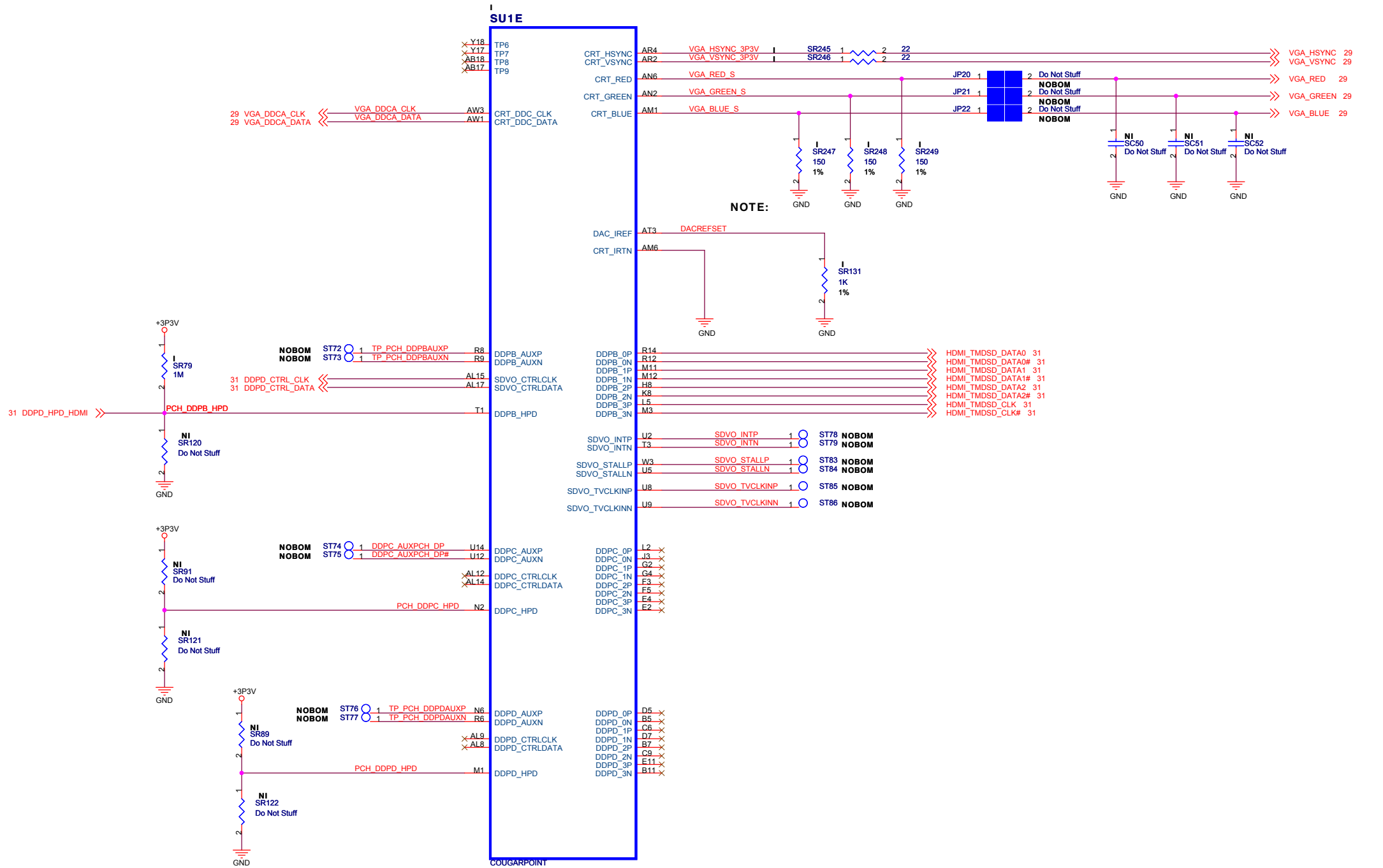
Date: **Wednesday, December 08, 2010** Sheet **18** of **76**

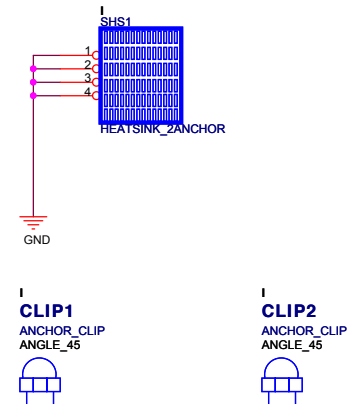
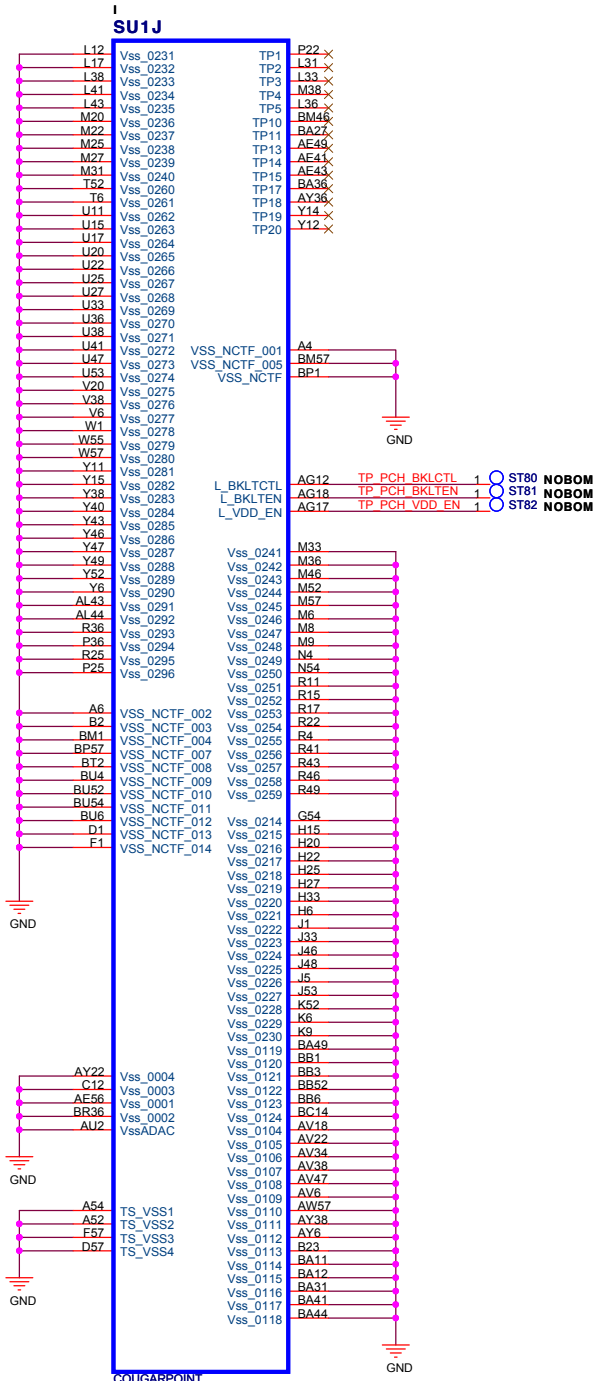
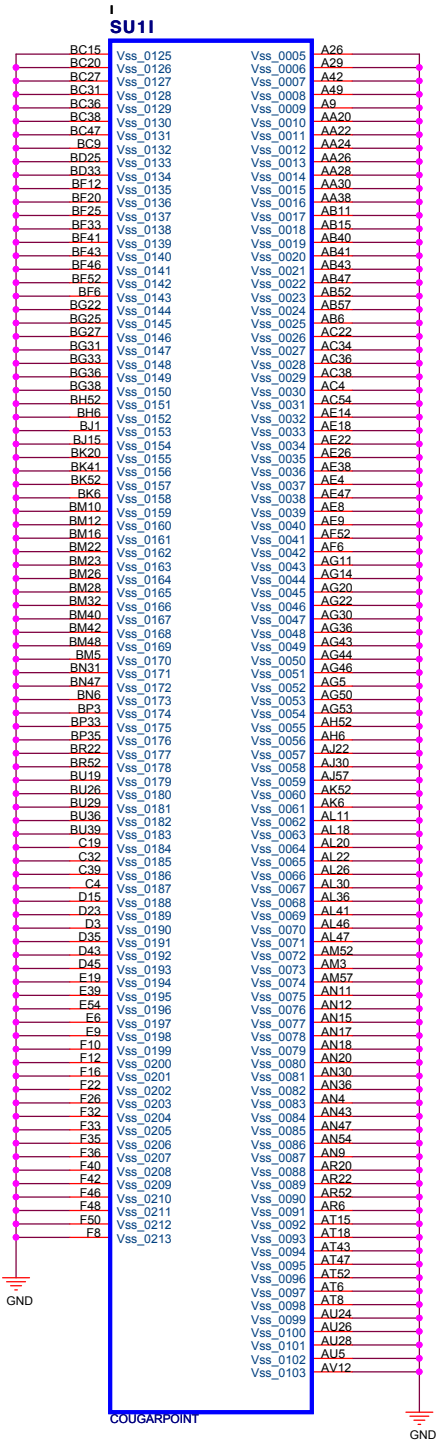


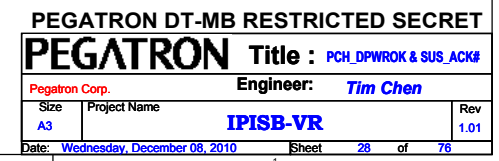


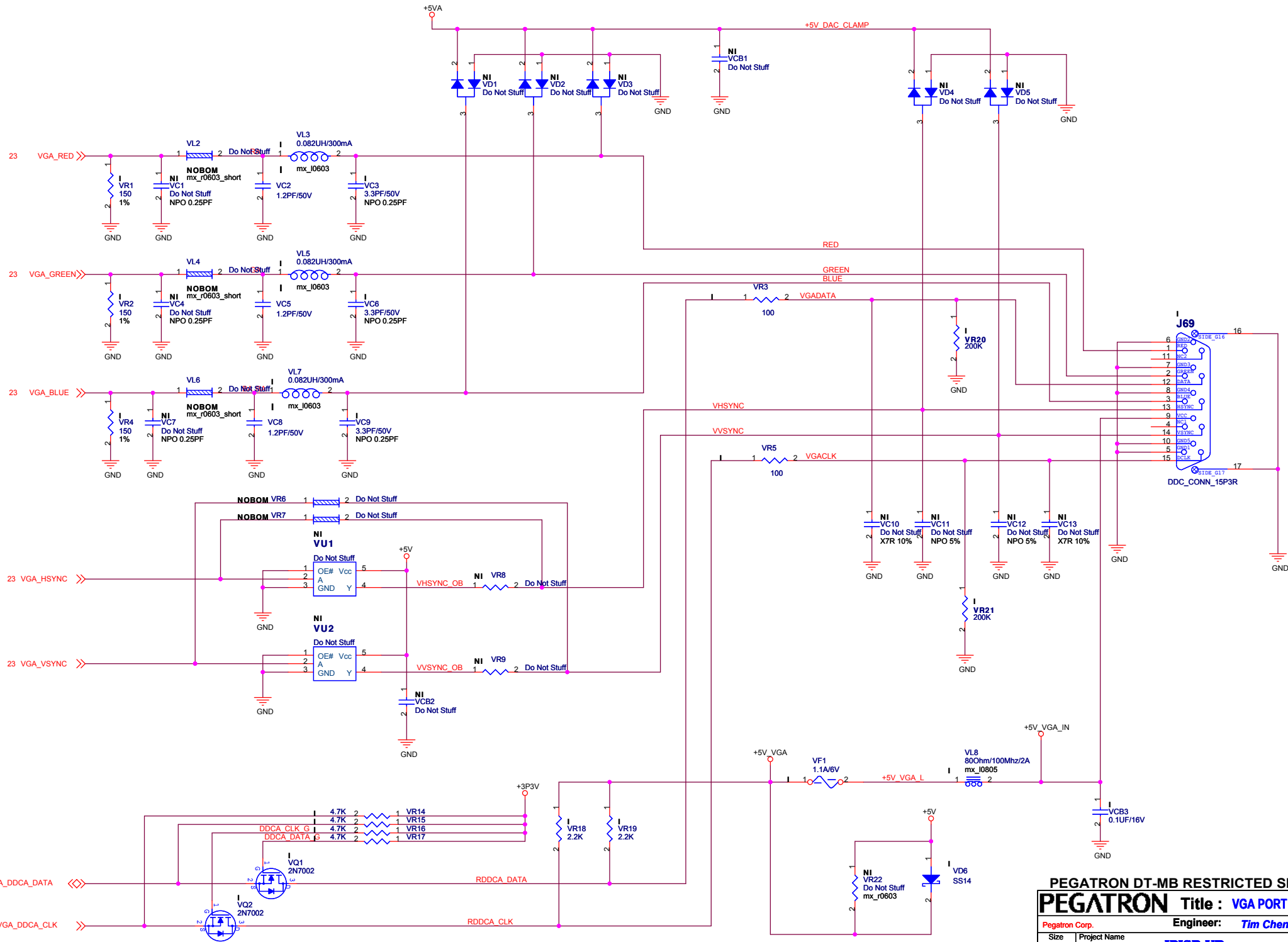
BOARD ID SEL

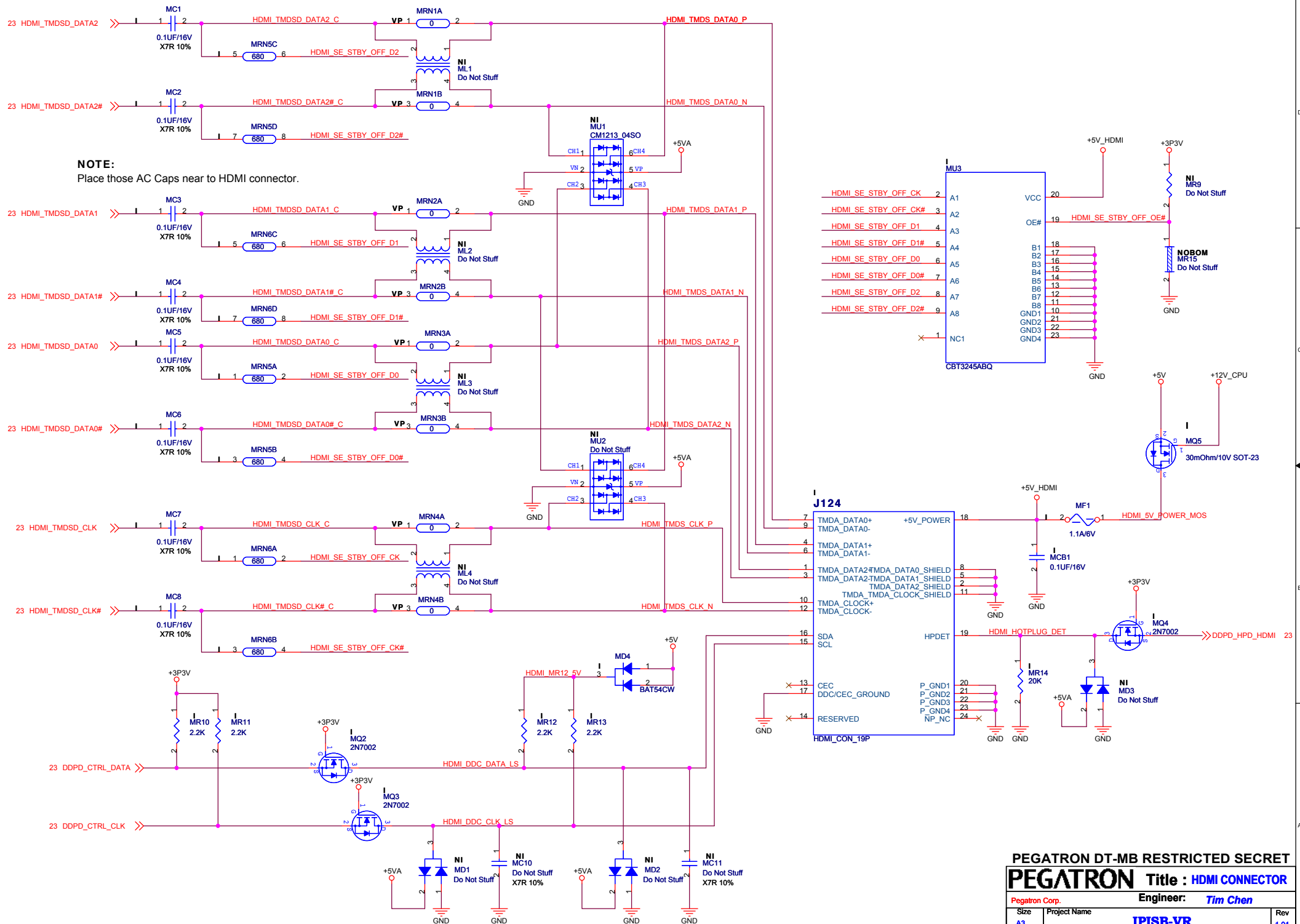


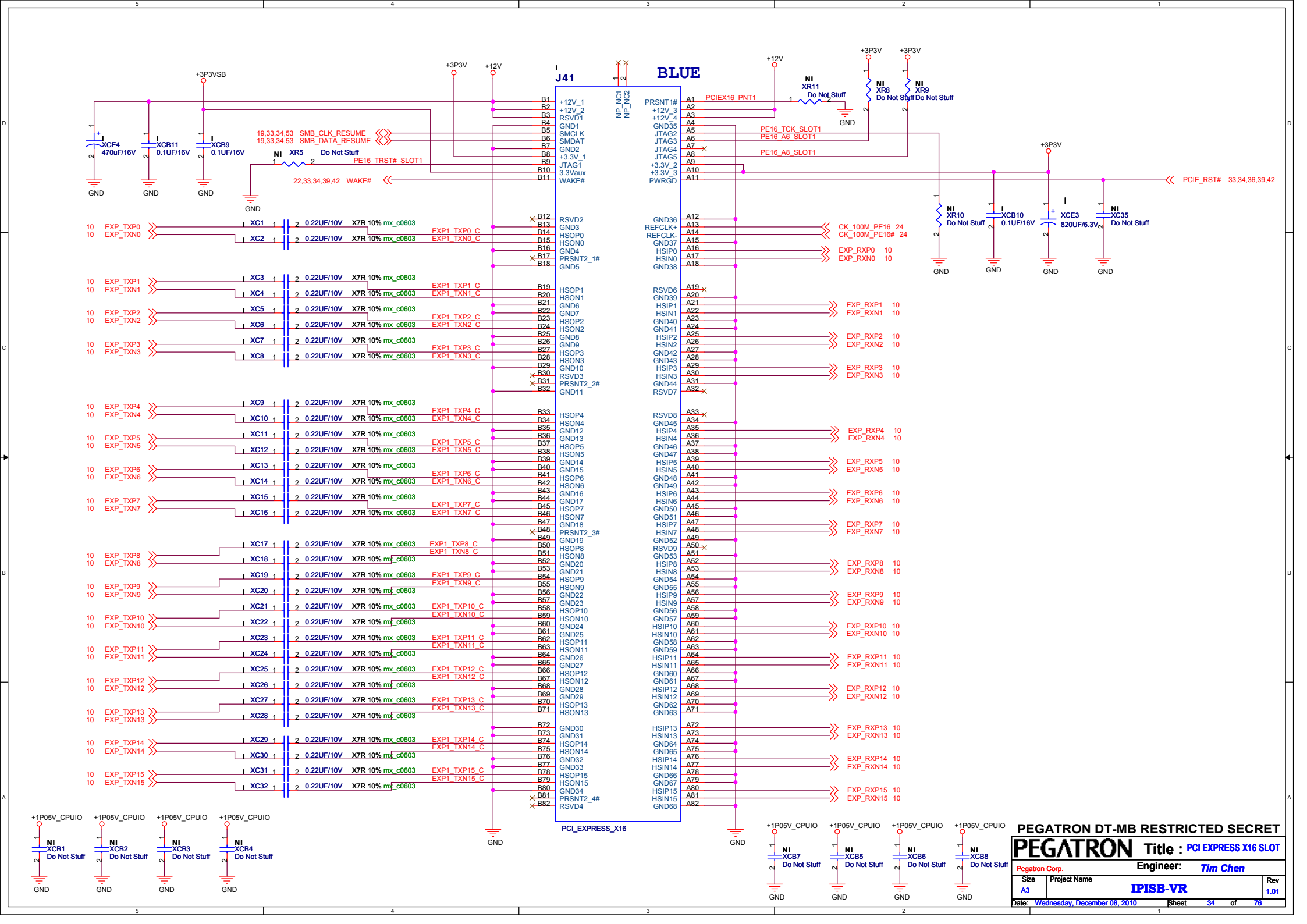




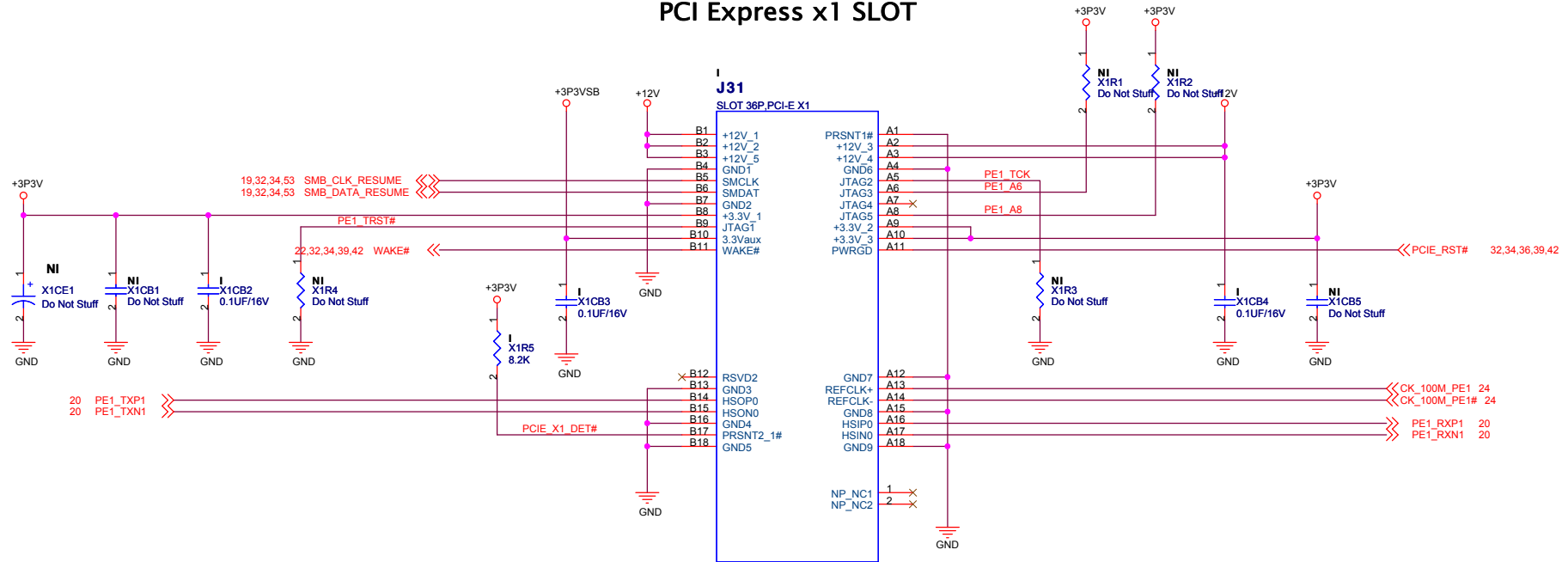


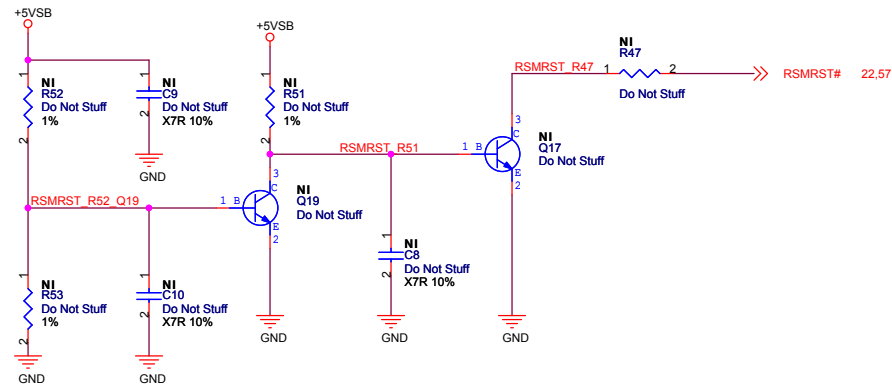
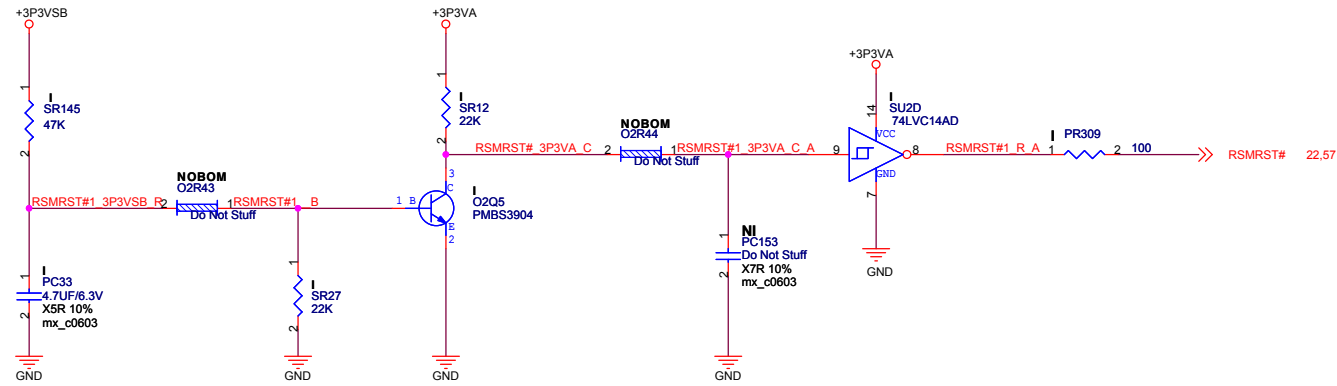






PCI Express x1 SLOT

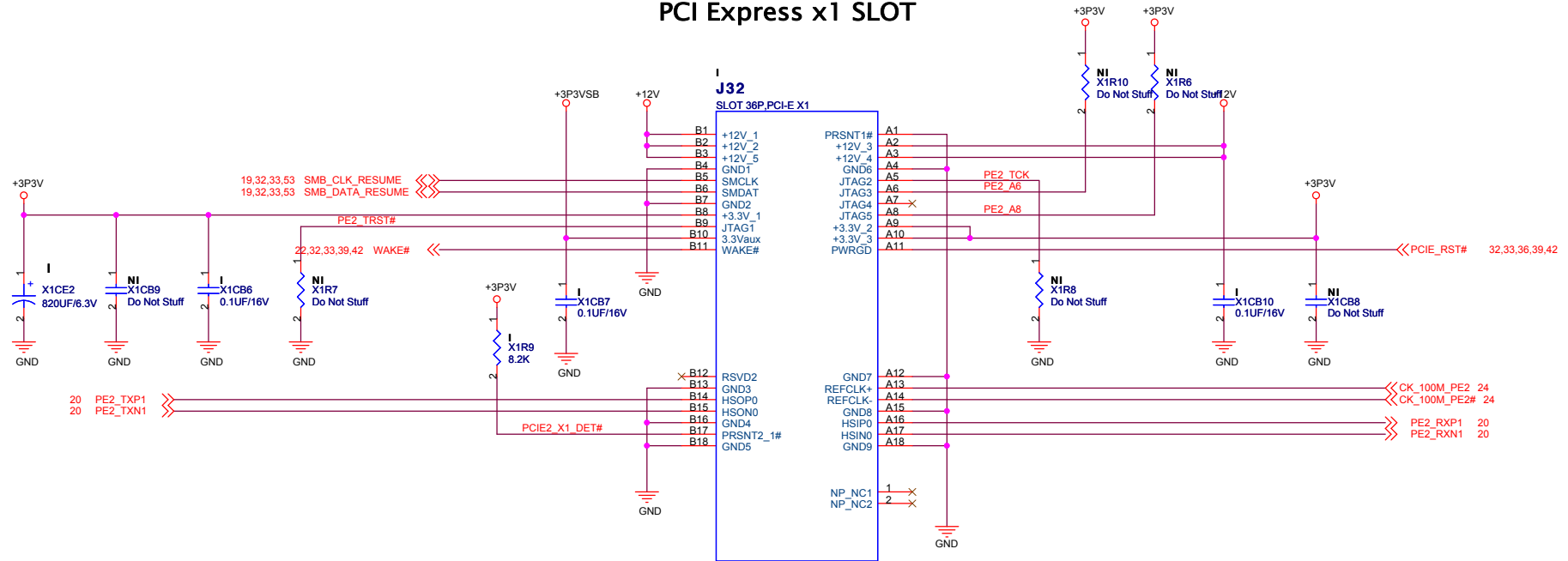




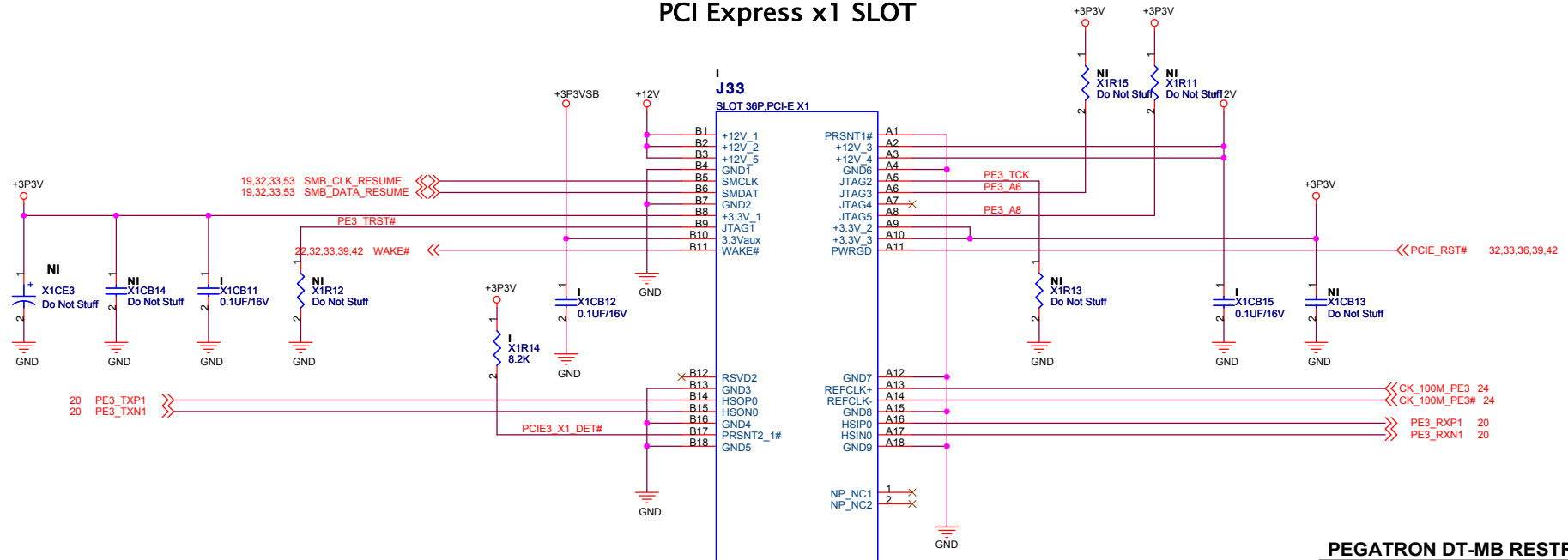
67R101A

PEGATRON		Title : RSMRST CIRCUIT	
PEGATRON CORP.		Engineer: Tim Chen	
Size	Project Name	Rev	
A3	IPISB-VR	1.01	
Date: Wednesday, December 08, 2010		Sheet	35 of 76

PCI Express x1 SLOT



PCI Express x1 SLOT



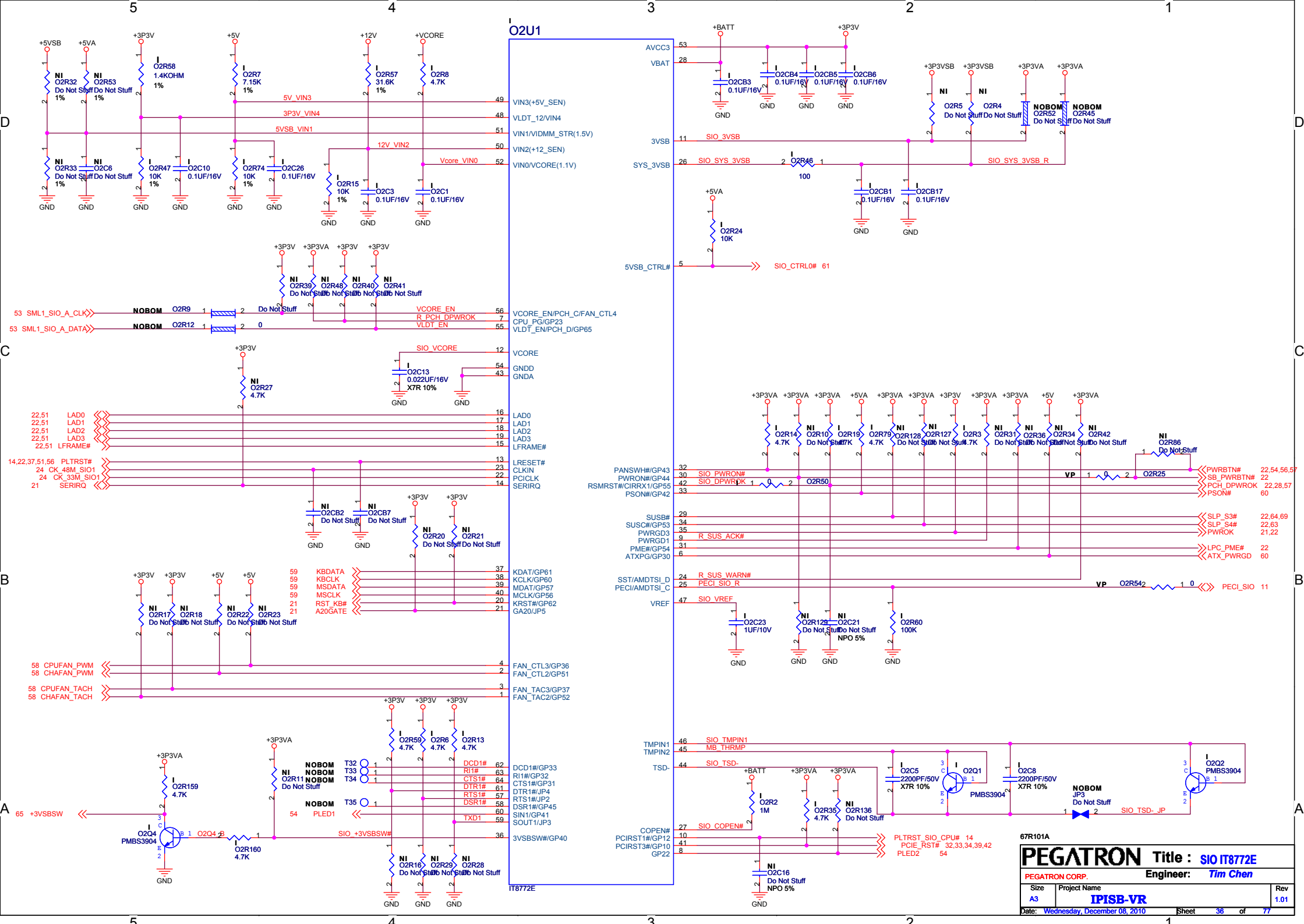
PEGATRON DT-MB RESTRICTED SECRET

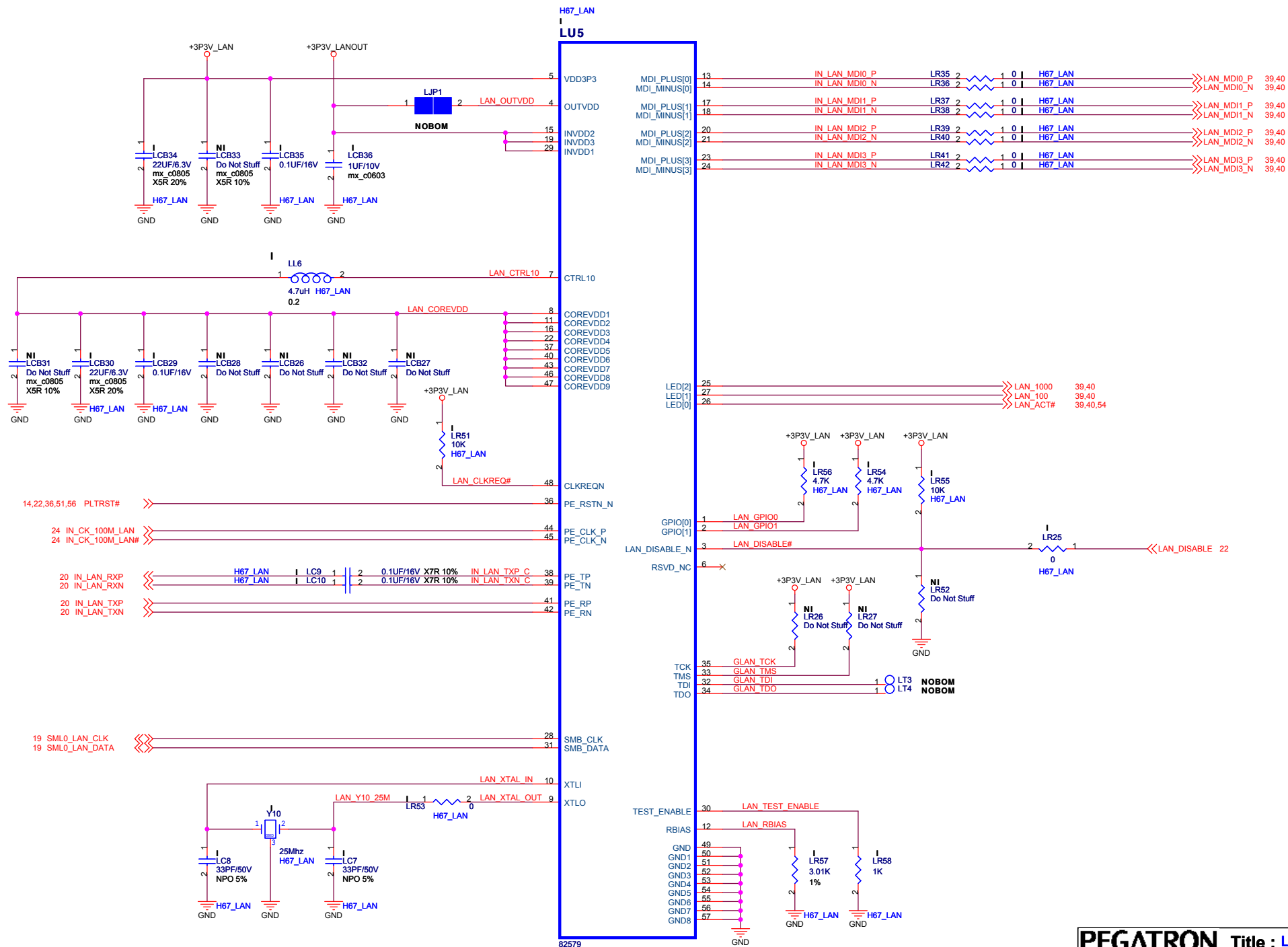
PEGATRON Title : PCI SLOT

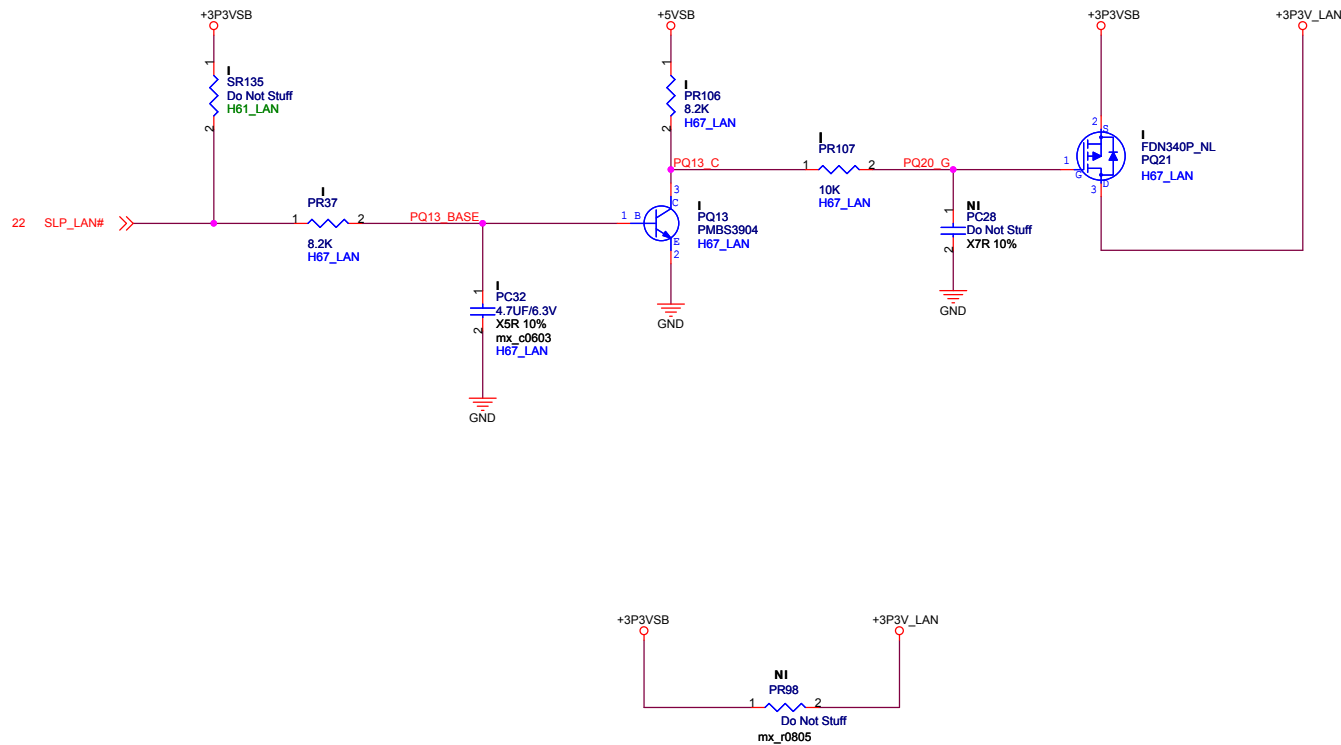
Pegatron Corp. Engineer: Tim Chen

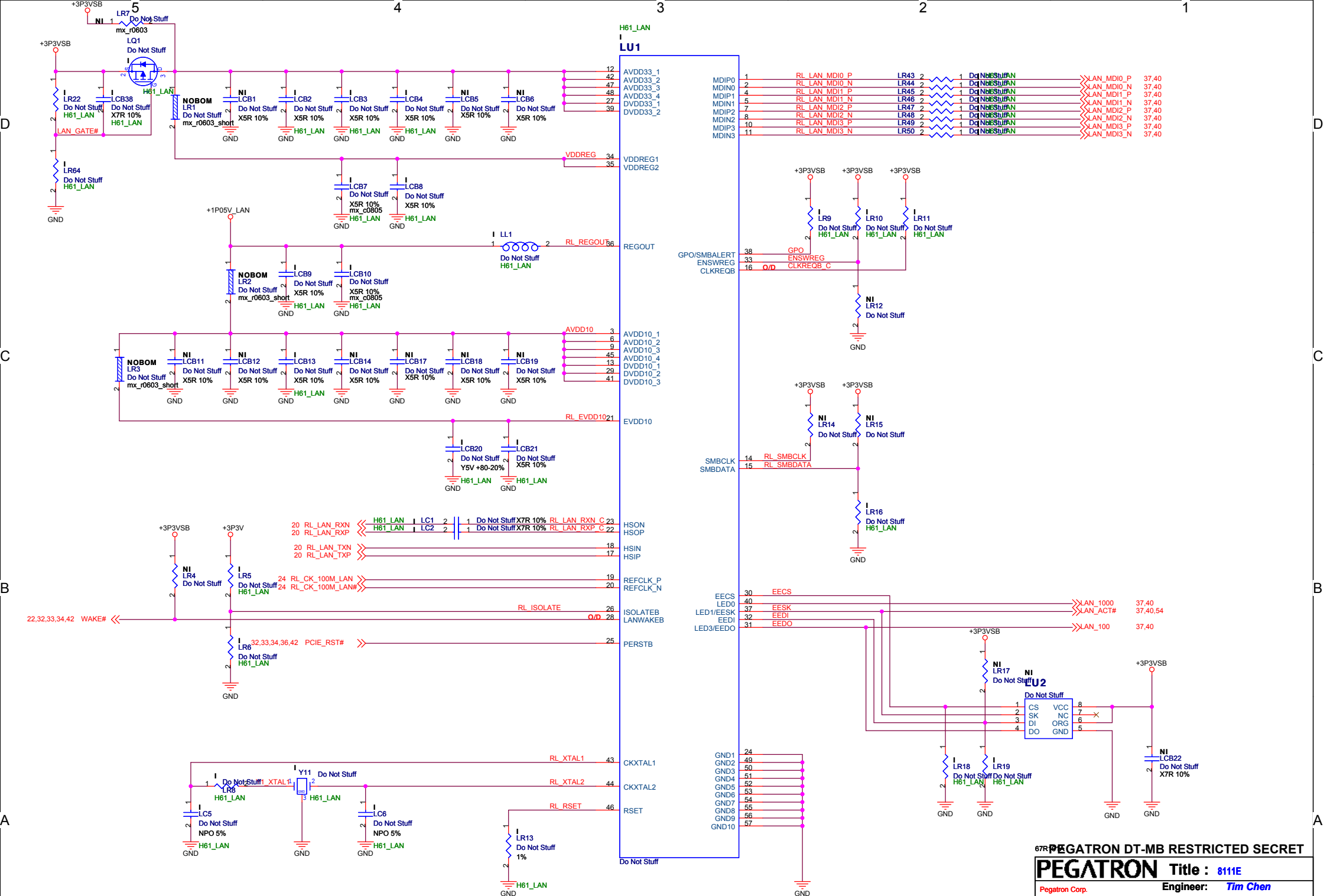
Size	Project Name	Rev
A3	IPISB-VR	1.01

Date: Wednesday, December 08, 2010 Sheet 36 of 76





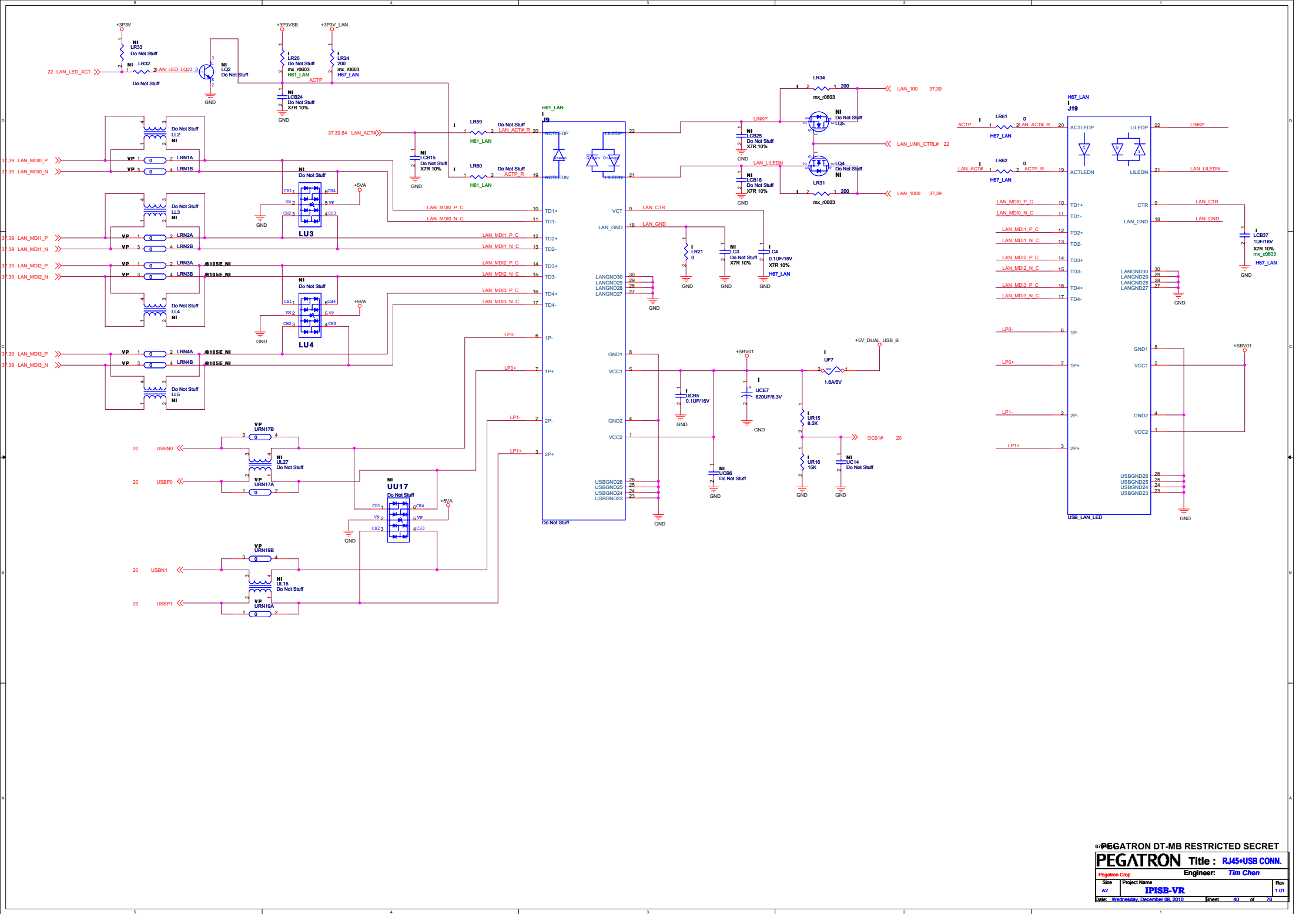


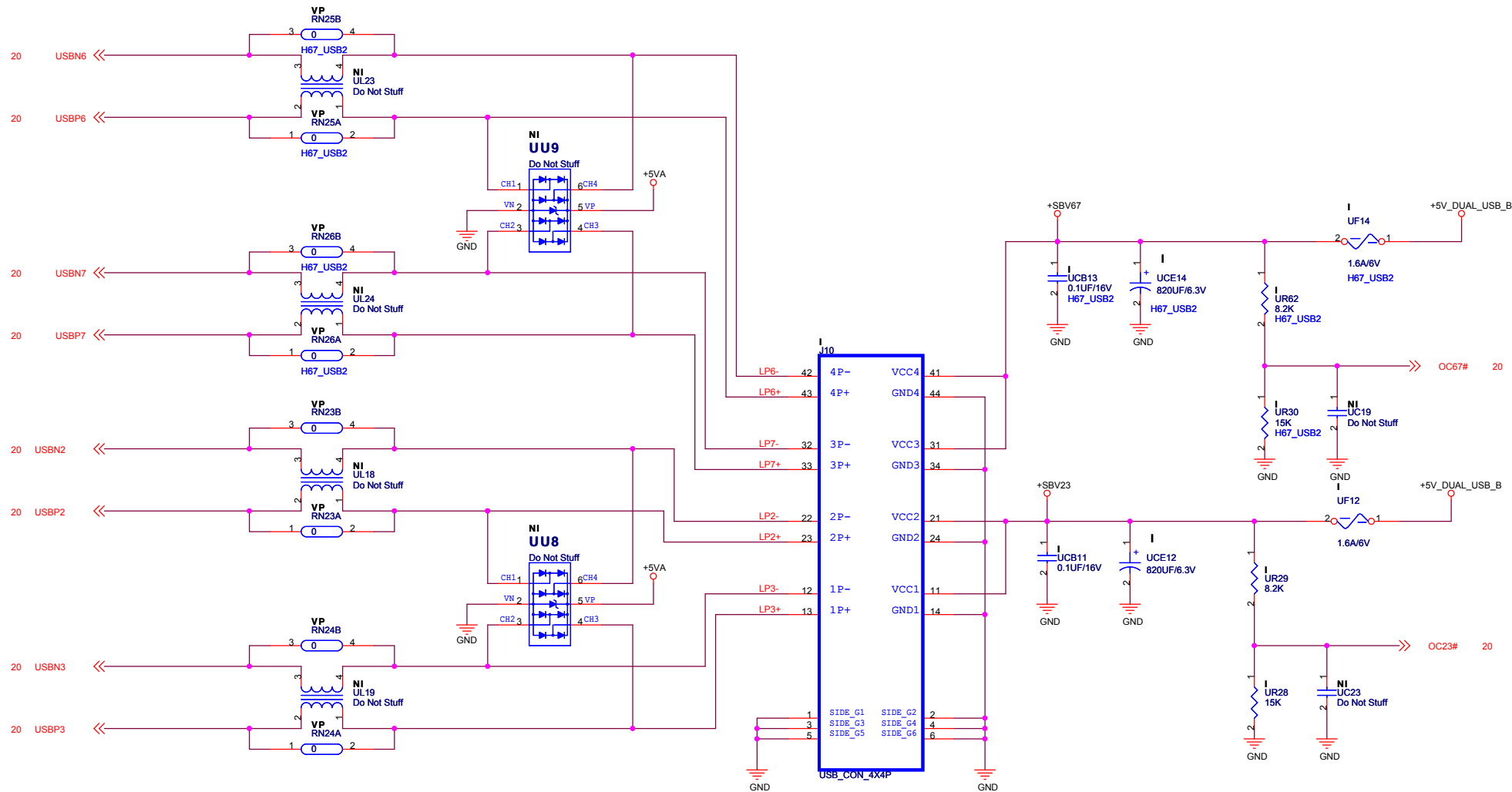


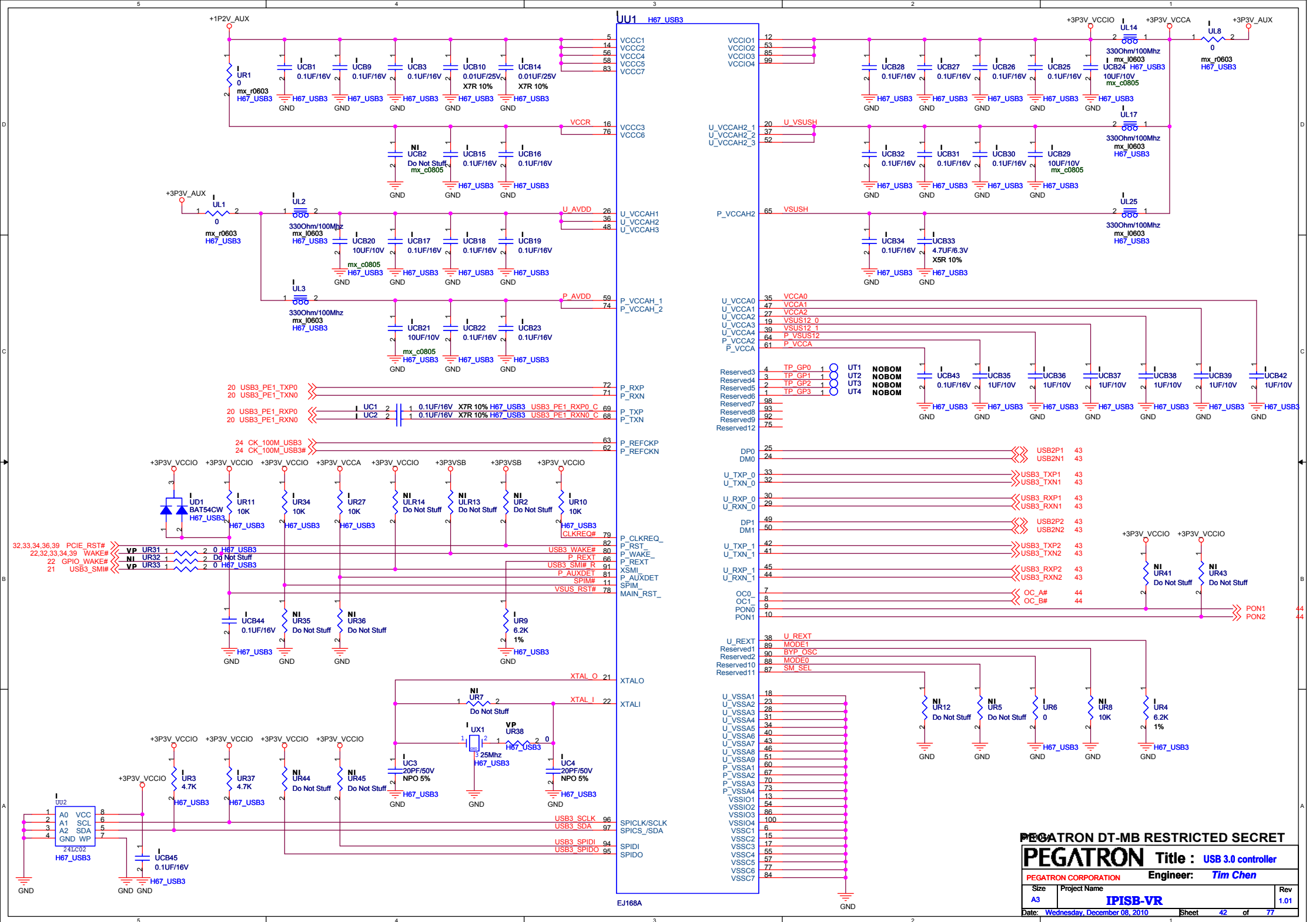
67R PEGATRON DT-MB RESTRICTED SECRET

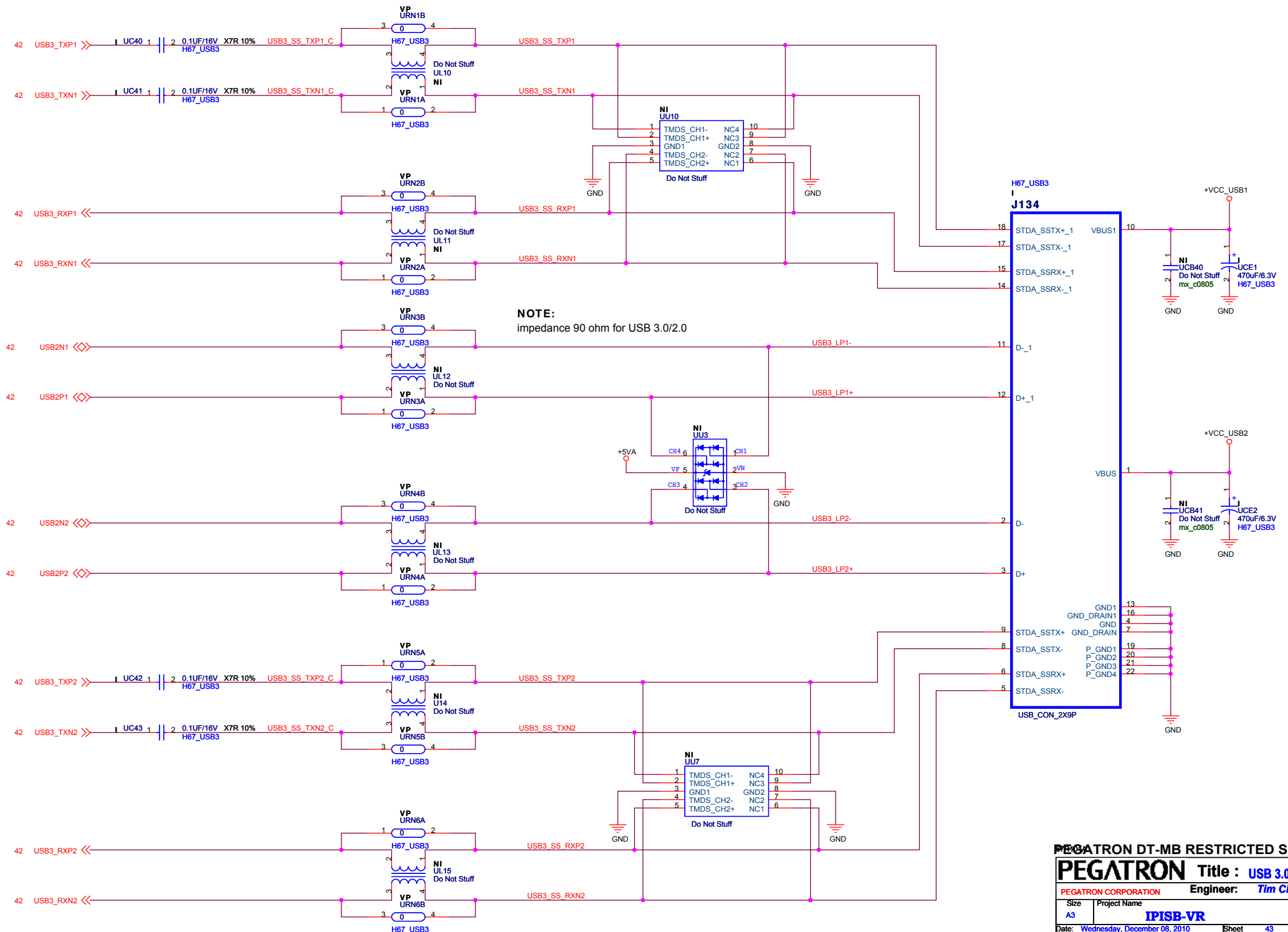
PEGATRON Title : 8111E
Pegatron Corp. Engineer: Tim Chen

Size A3	Project Name IPI5B-VR	Rev 1.01
Date: Wednesday, December 08, 2010 Sheet 39 of 76		

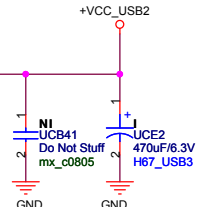
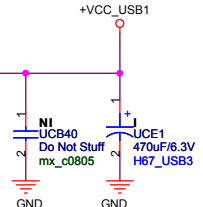
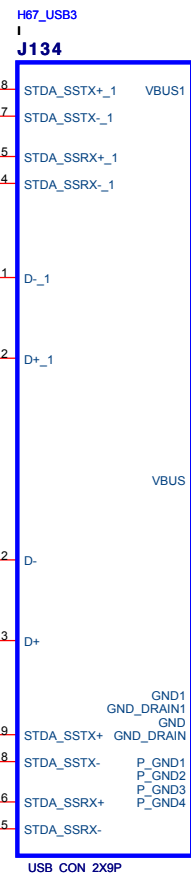


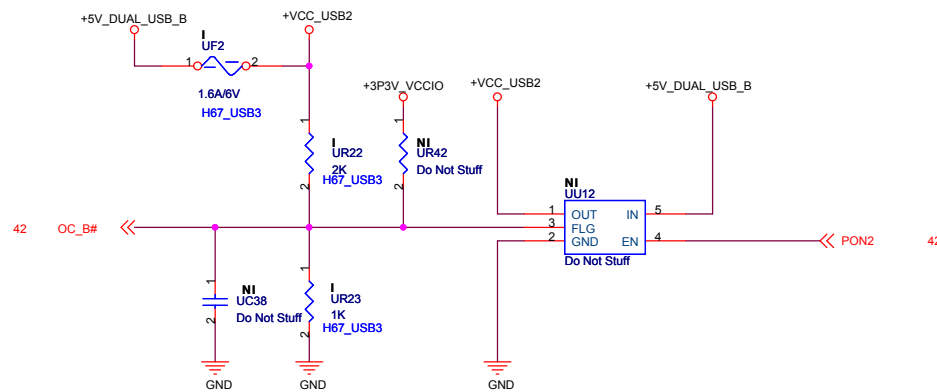
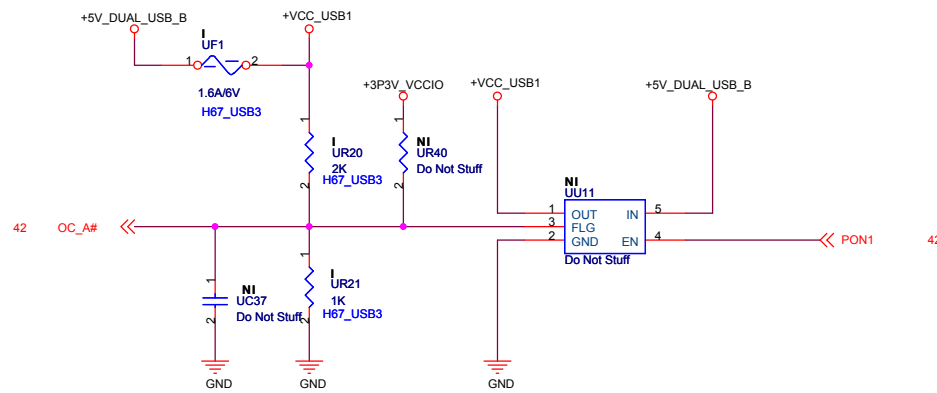


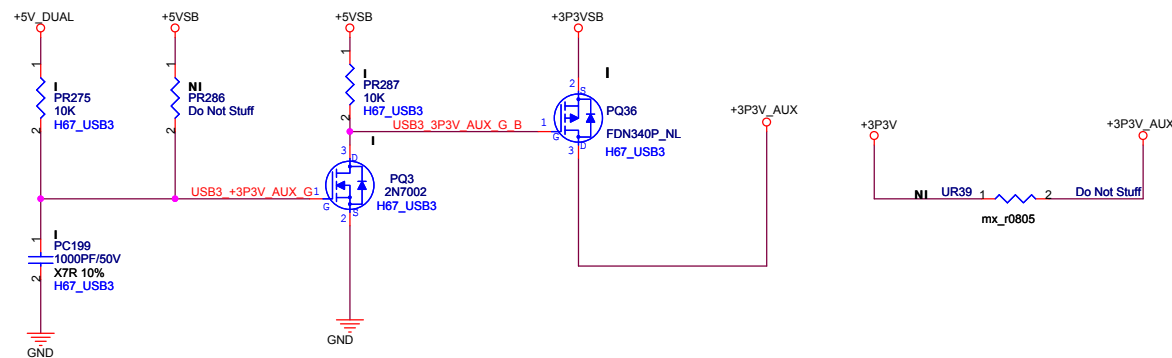
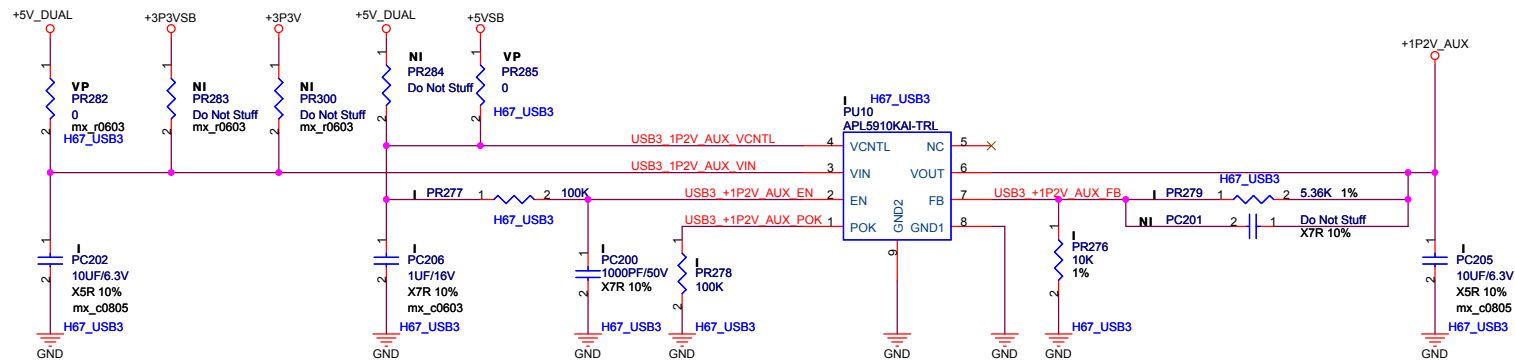


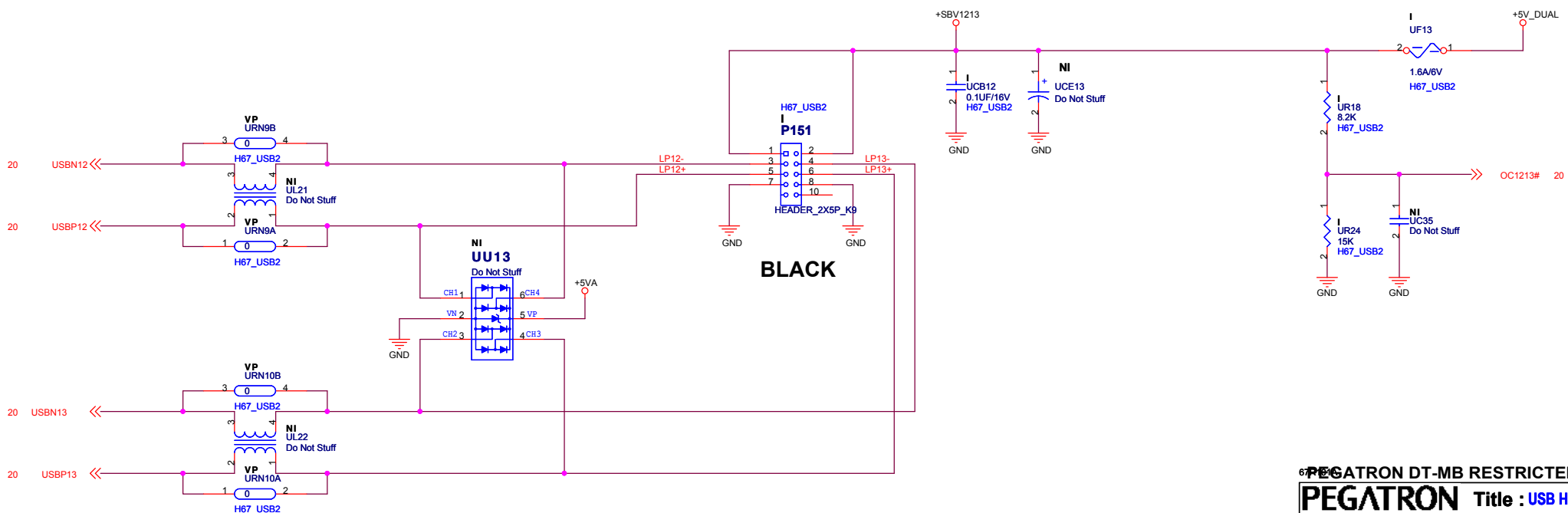
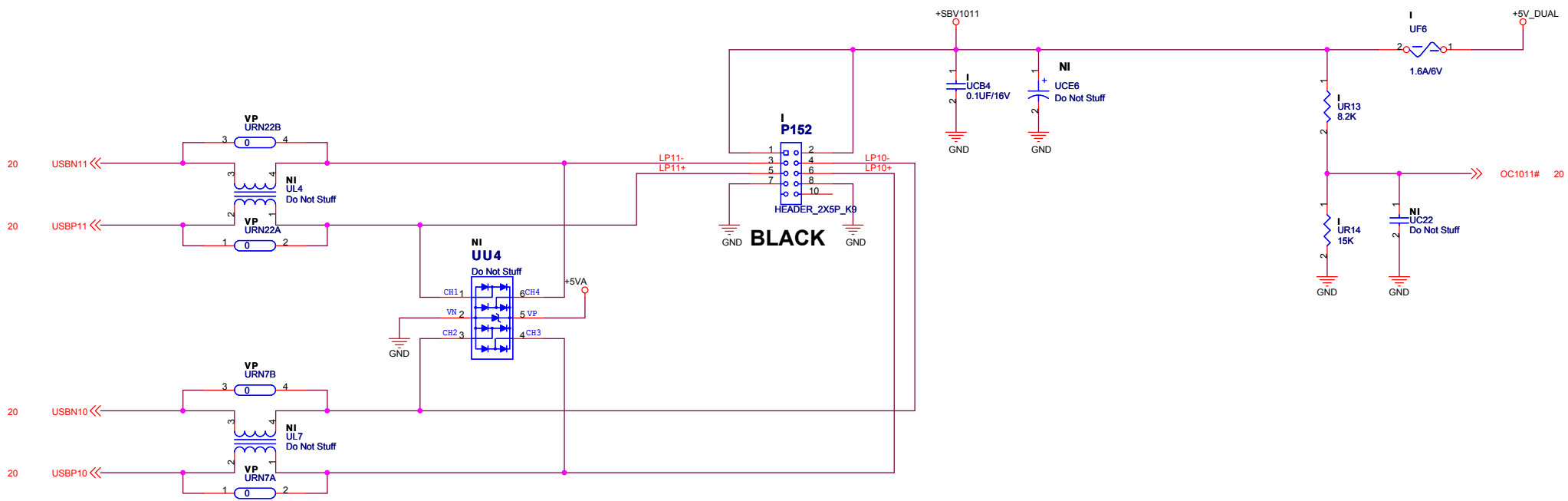


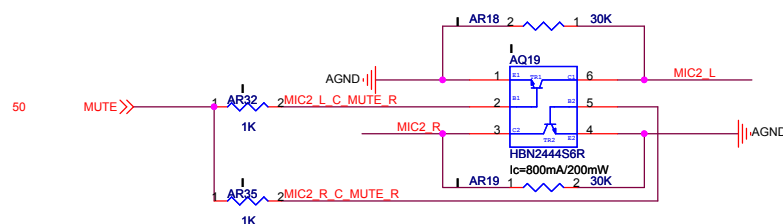
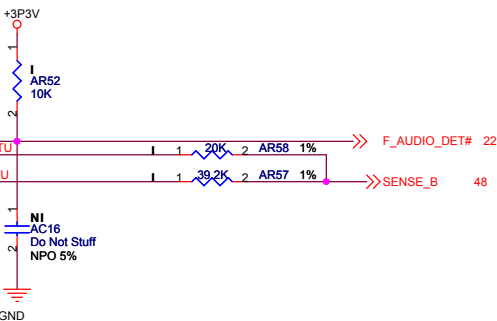
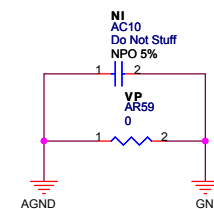
NOTE:
impedance 90 ohm for USB 3.0/2.0

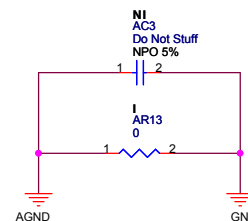


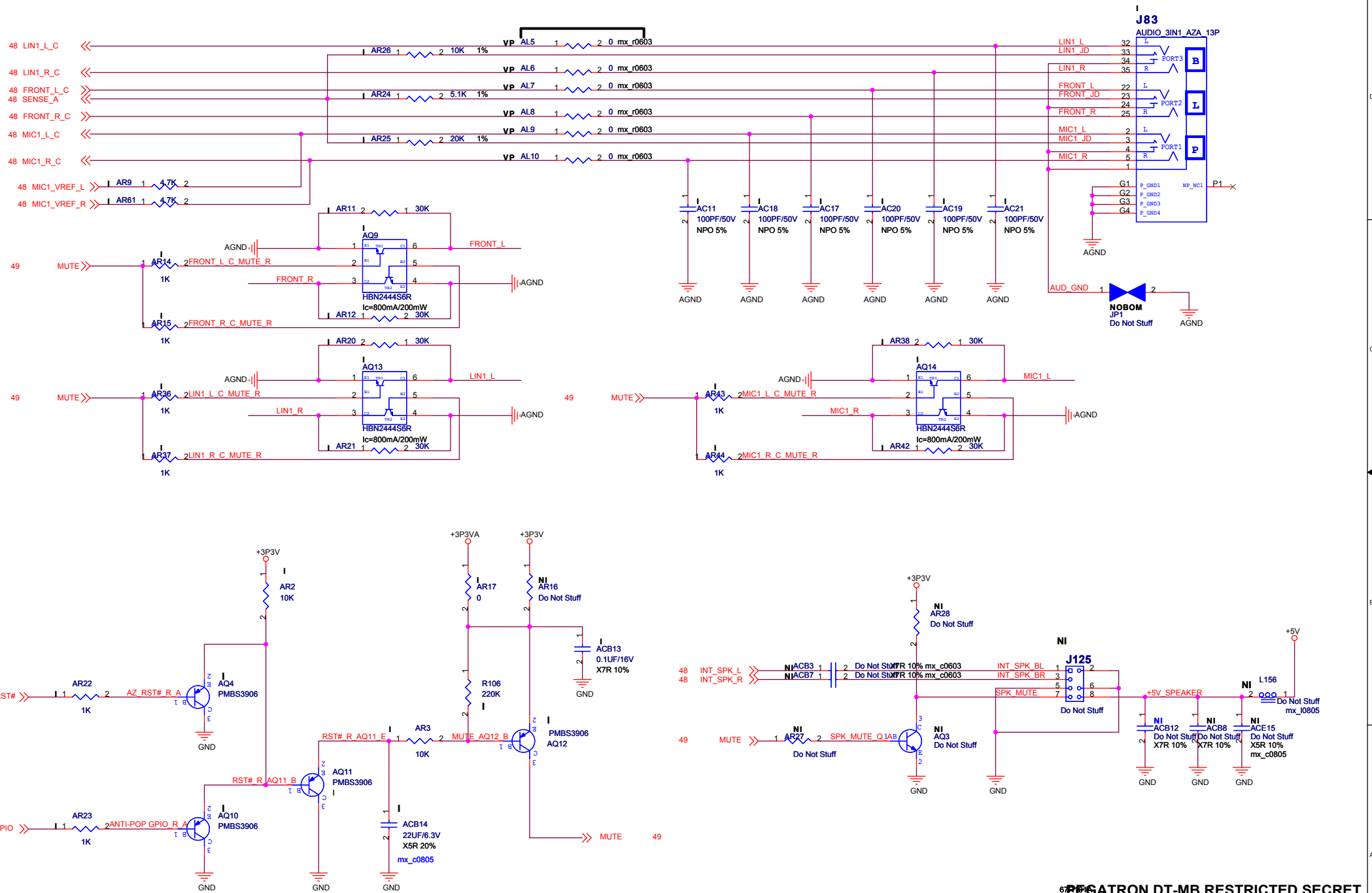




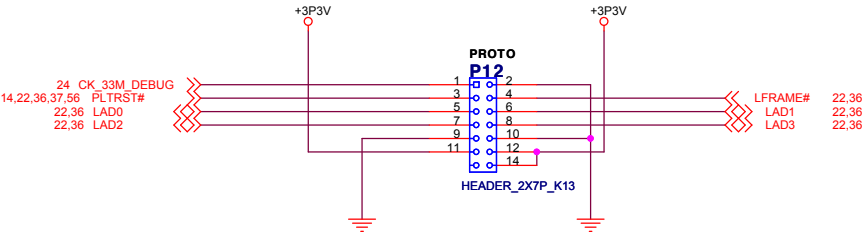




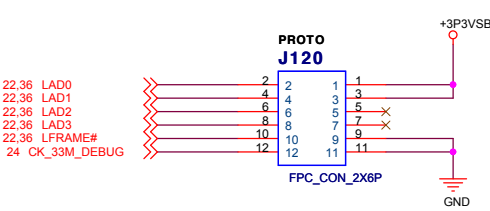




LPC DEBUG1



LPC DEBUG2



PEGATRON DT-MB RESTRICTED SECRET

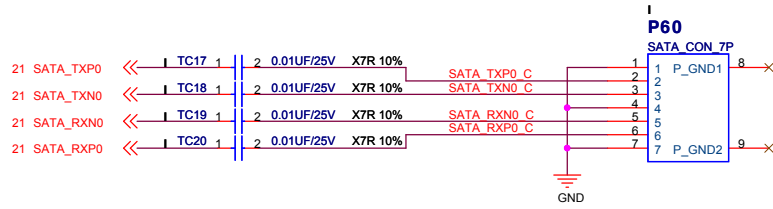
PEGATRON Title : **DEBUG/APS HEADER**

Pegatron Corp. Engineer: **Tim Chen**

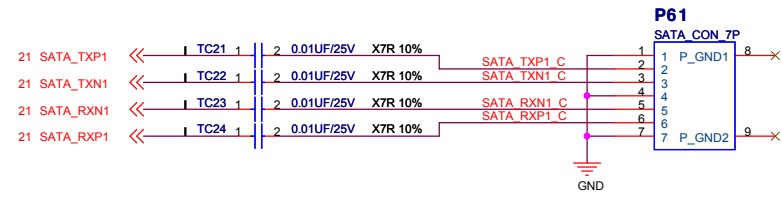
Size	Project Name	Rev
A3	IPISB-VR	1.01

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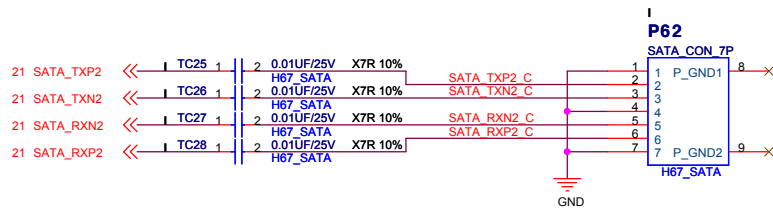
SATA CONNECTOR



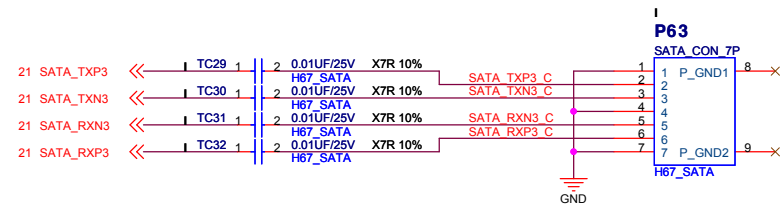
**SATA CONTROLLER #1
(PRIMARY MASTER)**
COLOR = BLACK



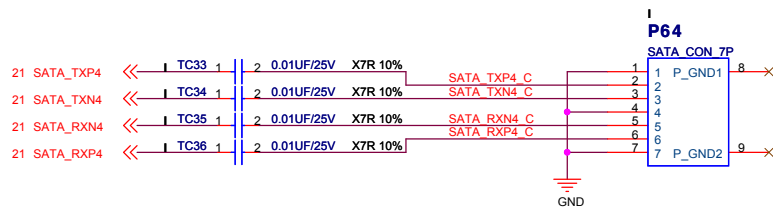
**SATA CONTROLLER #1
(SECONDARY MASTER)**
COLOR = BLACK



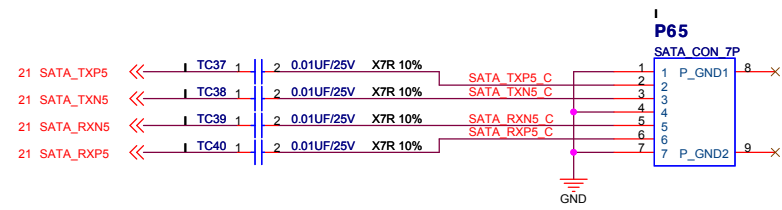
**SATA CONTROLLER #2
(PRIMARY MASTER)**
COLOR = BLACK



**SATA CONTROLLER #2
(SECONDARY MASTER)**
COLOR = BLACK



**SATA CONTROLLER #3
(PRIMARY MASTER)**
COLOR = BLACK



**SATA CONTROLLER #3
(SECONDARY MASTER)**
COLOR = BLACK

PEGATRON DT-MB RESTRICTED SECRET

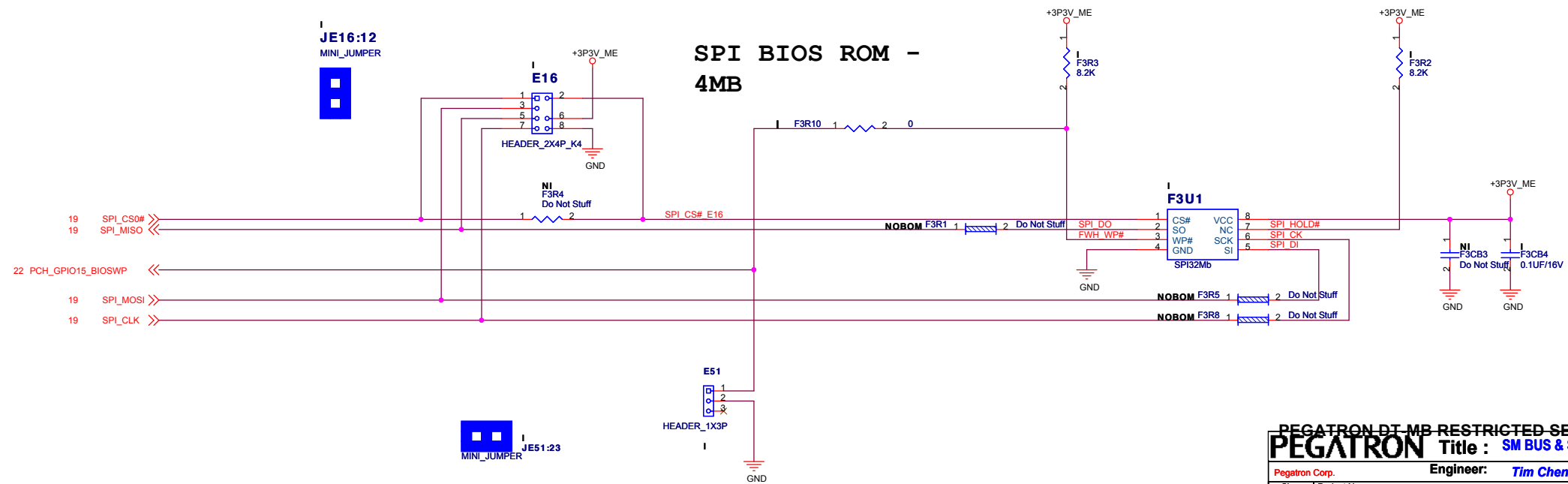
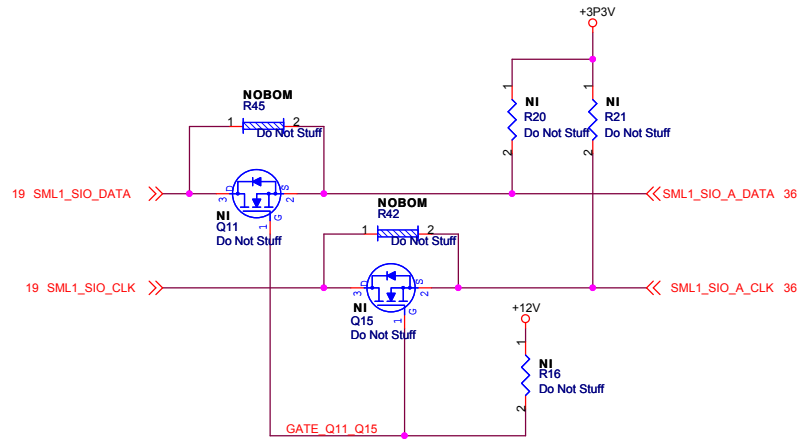
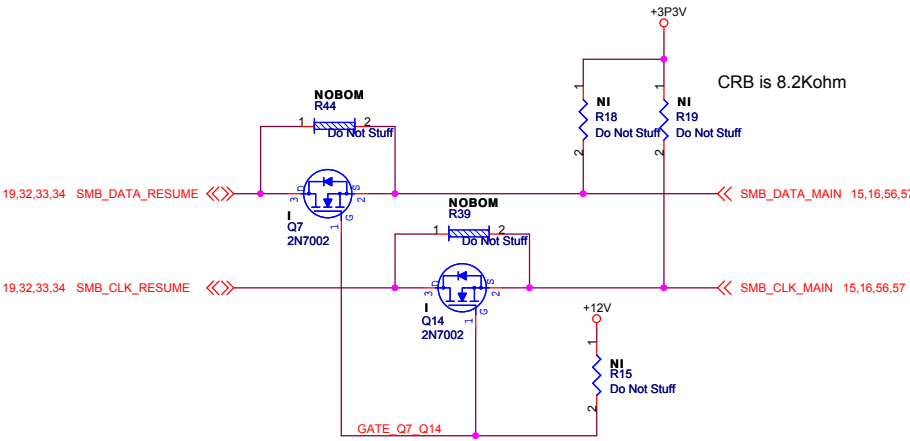
PEGATRON Title : **SATA CONNECTOR**

Pegatron Corp. Engineer: **Tim Chen**

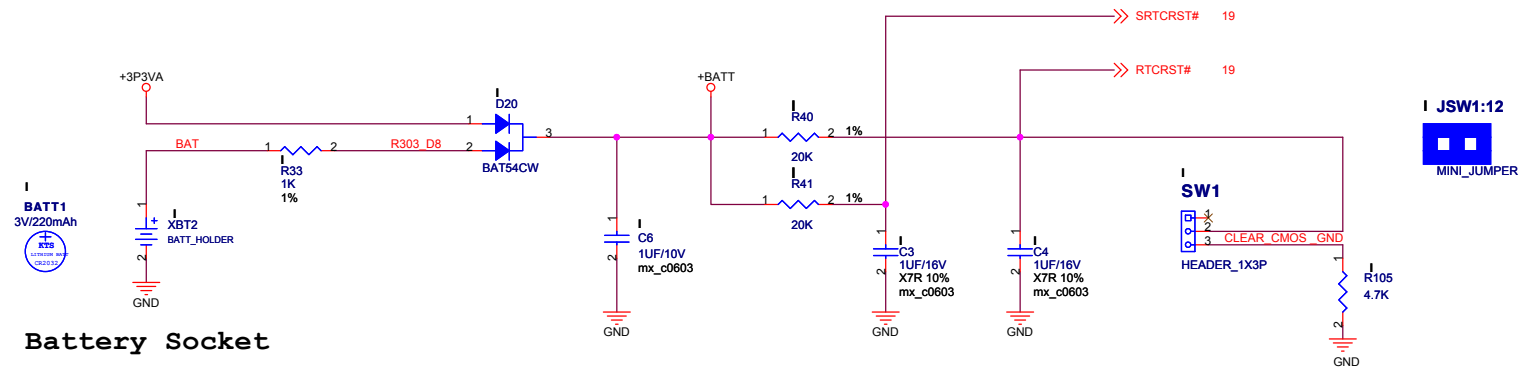
Size A3 Project Name **IPISB-VR** Rev 1.01

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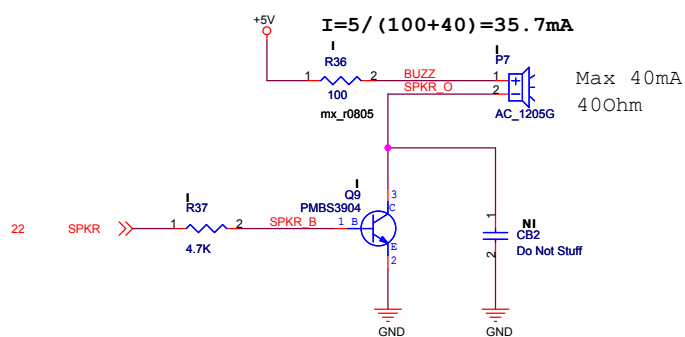
SM BUS Control



Battery Socket

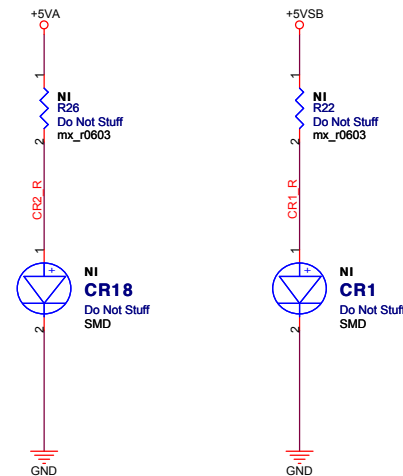


SPEAKER

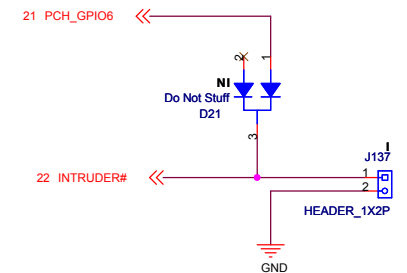


+5VA : BLUE

+5VSB : GREEN



Case Open



NOBOM
H1
Do Not Stuff

NOBOM
H2
Do Not Stuff

NOBOM
H3
Do Not Stuff

NOBOM
H4
Do Not Stuff

NOBOM
H5
Do Not Stuff

NOBOM
H6
Do Not Stuff

NOBOM
H7
Do Not Stuff

NOBOM
H8
Do Not Stuff

PEGATRON DT-MB RESTRICTED SECRET

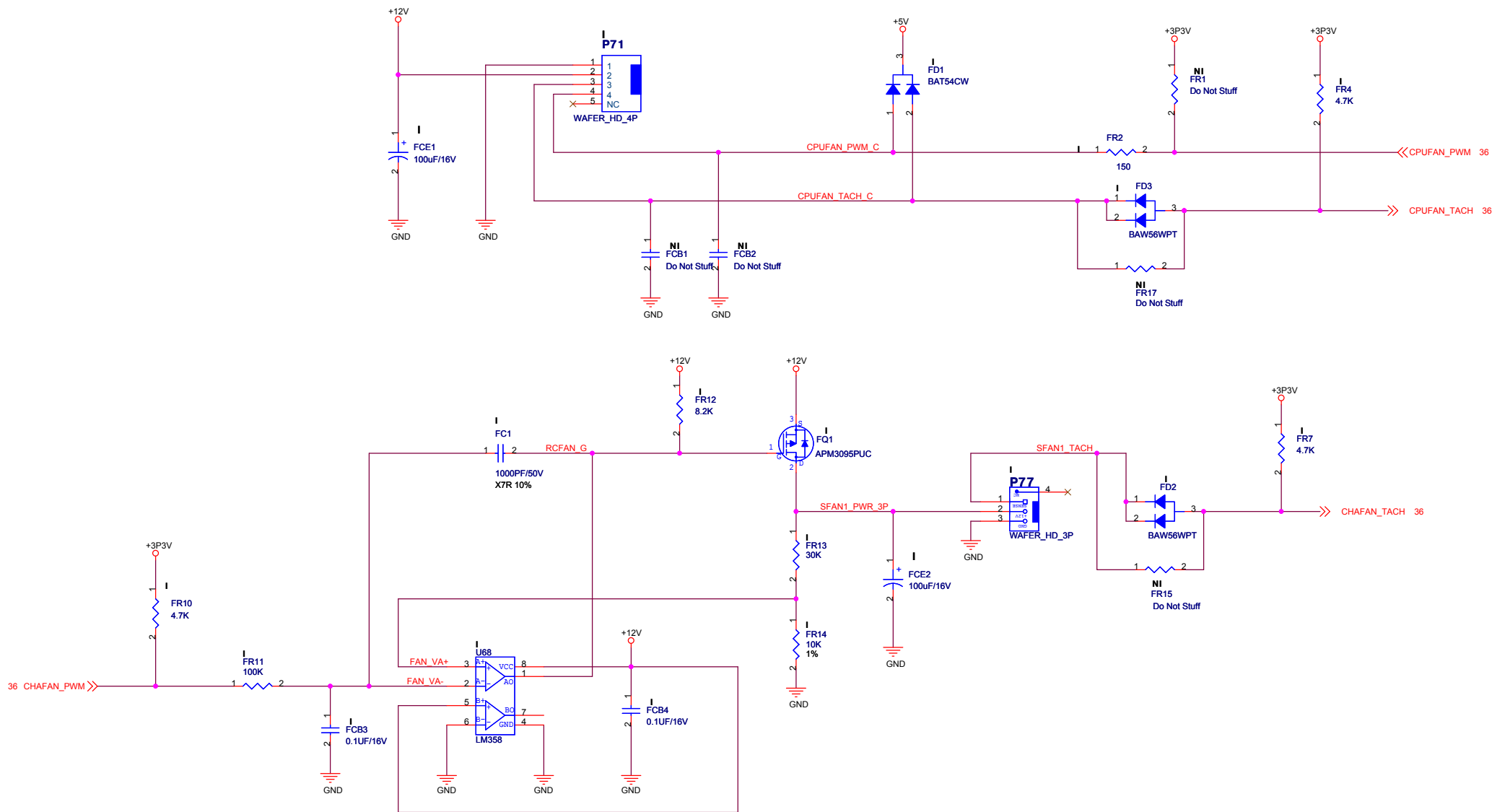
PEGATRON Title: C/COMS/SPKR/SCREW

Pegatron Corp. Engineer: Tim Chen

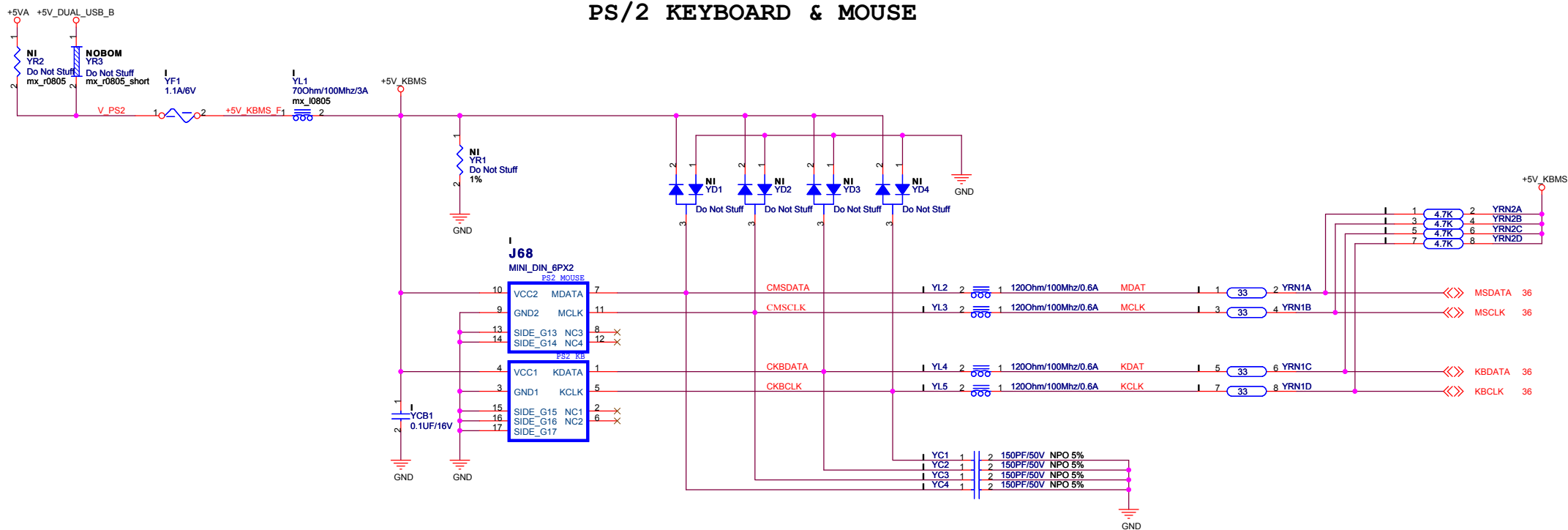
Size A3 Project Name IPISB-VR

Date: Wednesday, December 08, 2010 Sheet 55 of 76

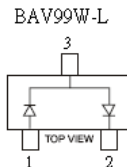
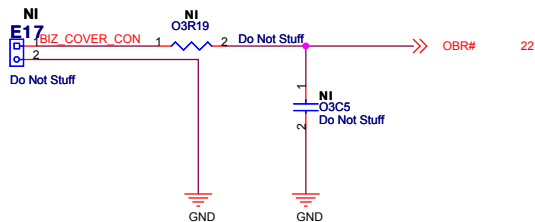
Rev 1.01



PS/2 KEYBOARD & MOUSE



Touch Recovery



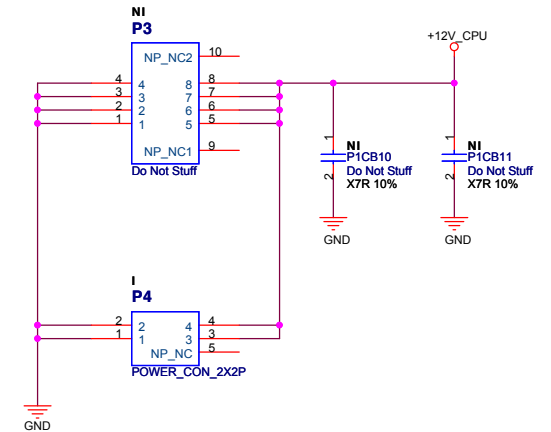
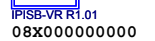
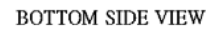
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title : PS/2 KB/MS

Pegatron Corp. Engineer: Tim Chen

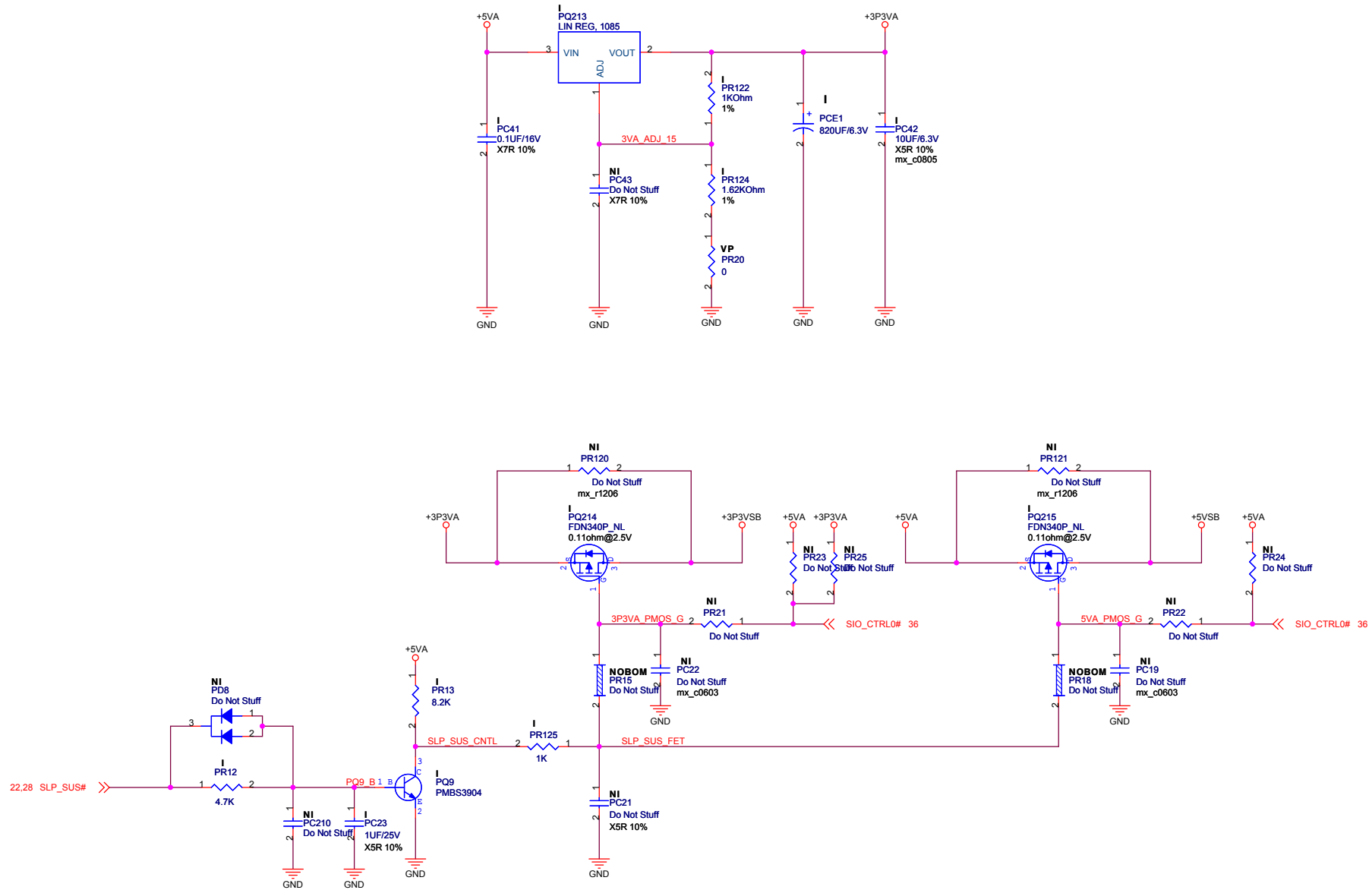
Size	Project Name	Rev
A3	IPISB-VR	1.01

Date: Wednesday, December 08, 2010 Sheet 59 of 76

**PEGATRON** Title : **ATX POWER_24P**

Size A3	Project Name IPISB-VR	Rev 1.01
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Date: Wednesday, December 08, 2010 Sheet 60 of 76



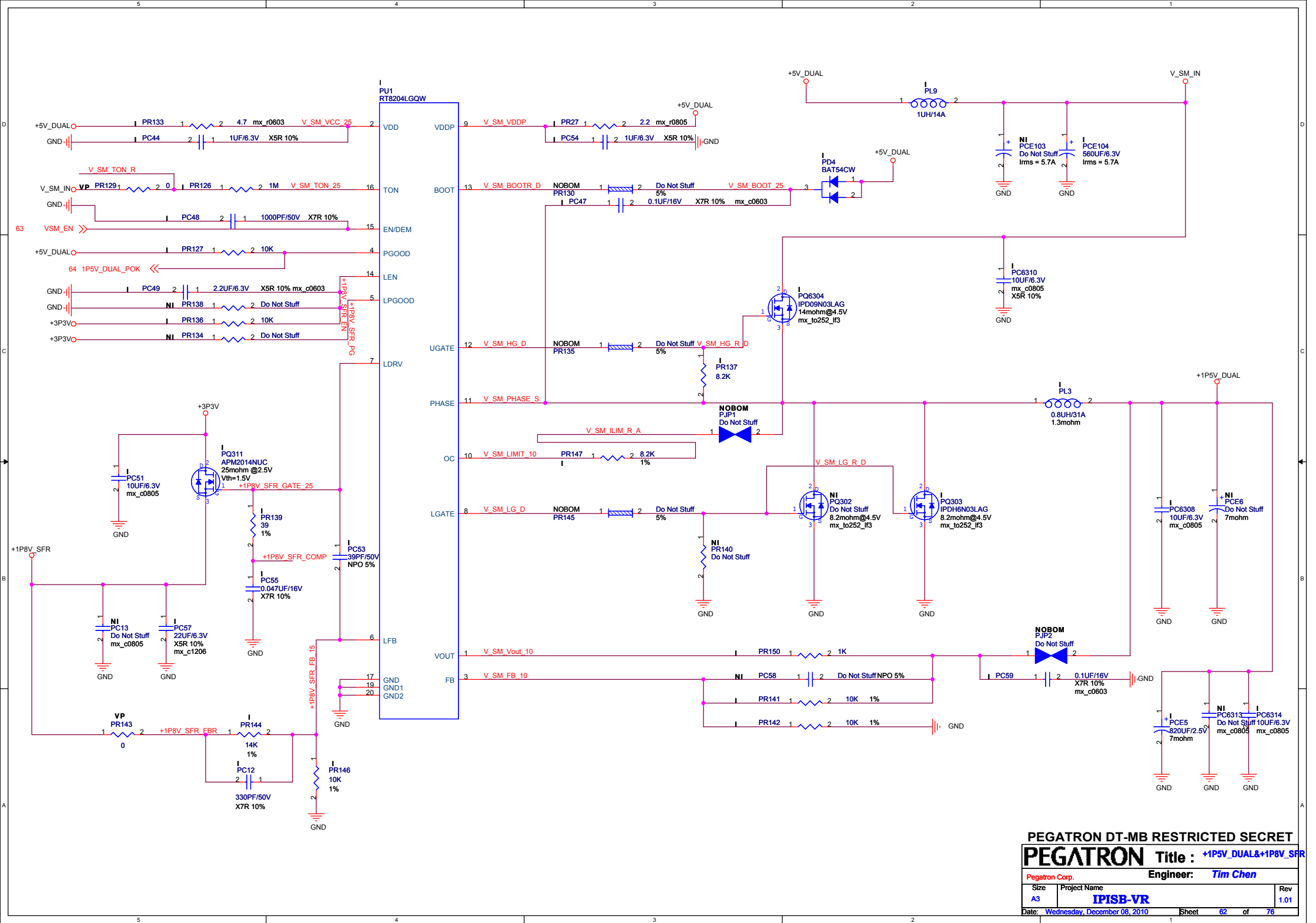
PEGATRON DT-MB RESTRICTED SECRET

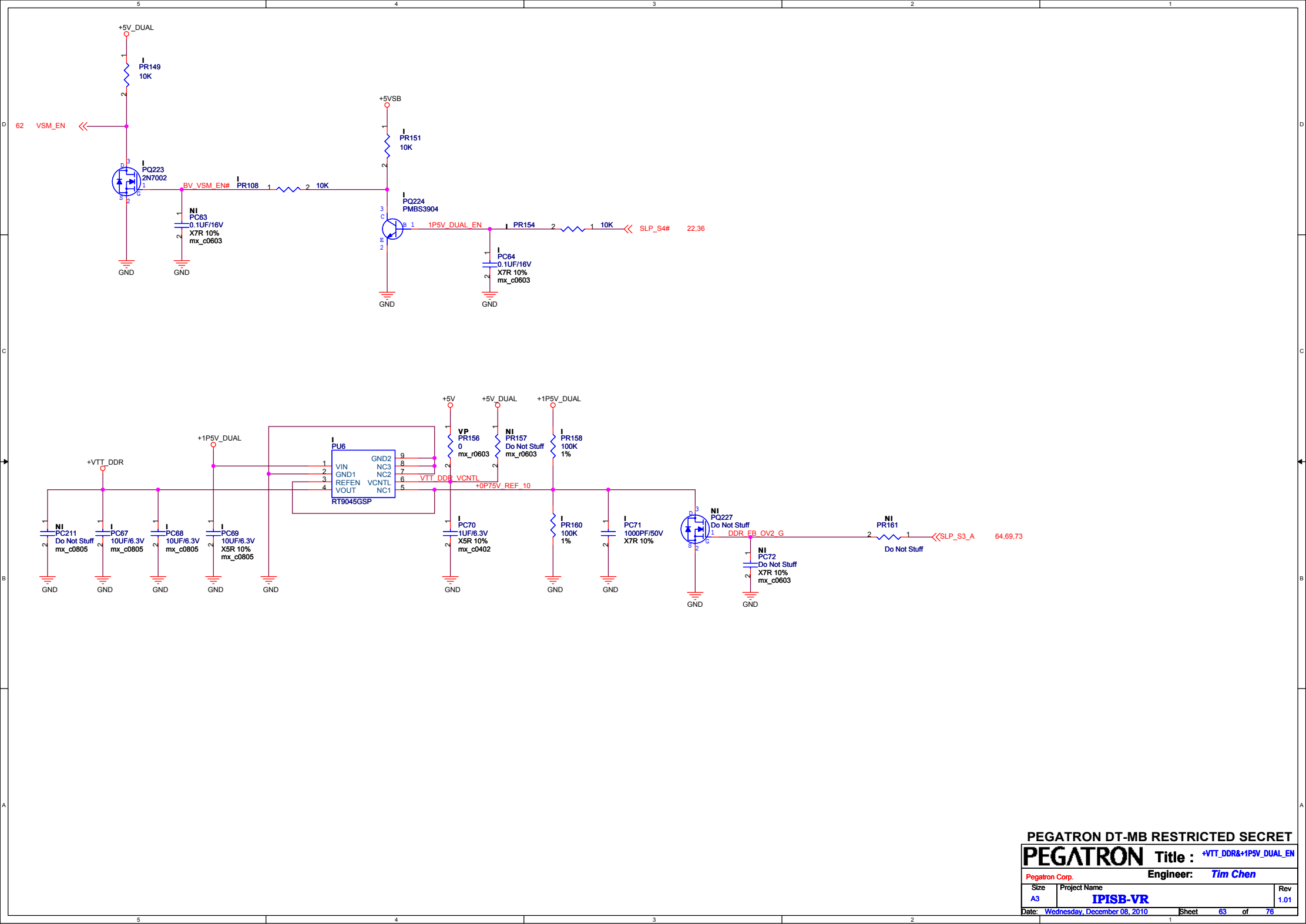
PEGATRON Title : +3VA&+3VSB&+5VSB

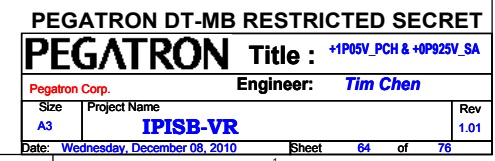
Pegatron Corp. Engineer: Tim Chen

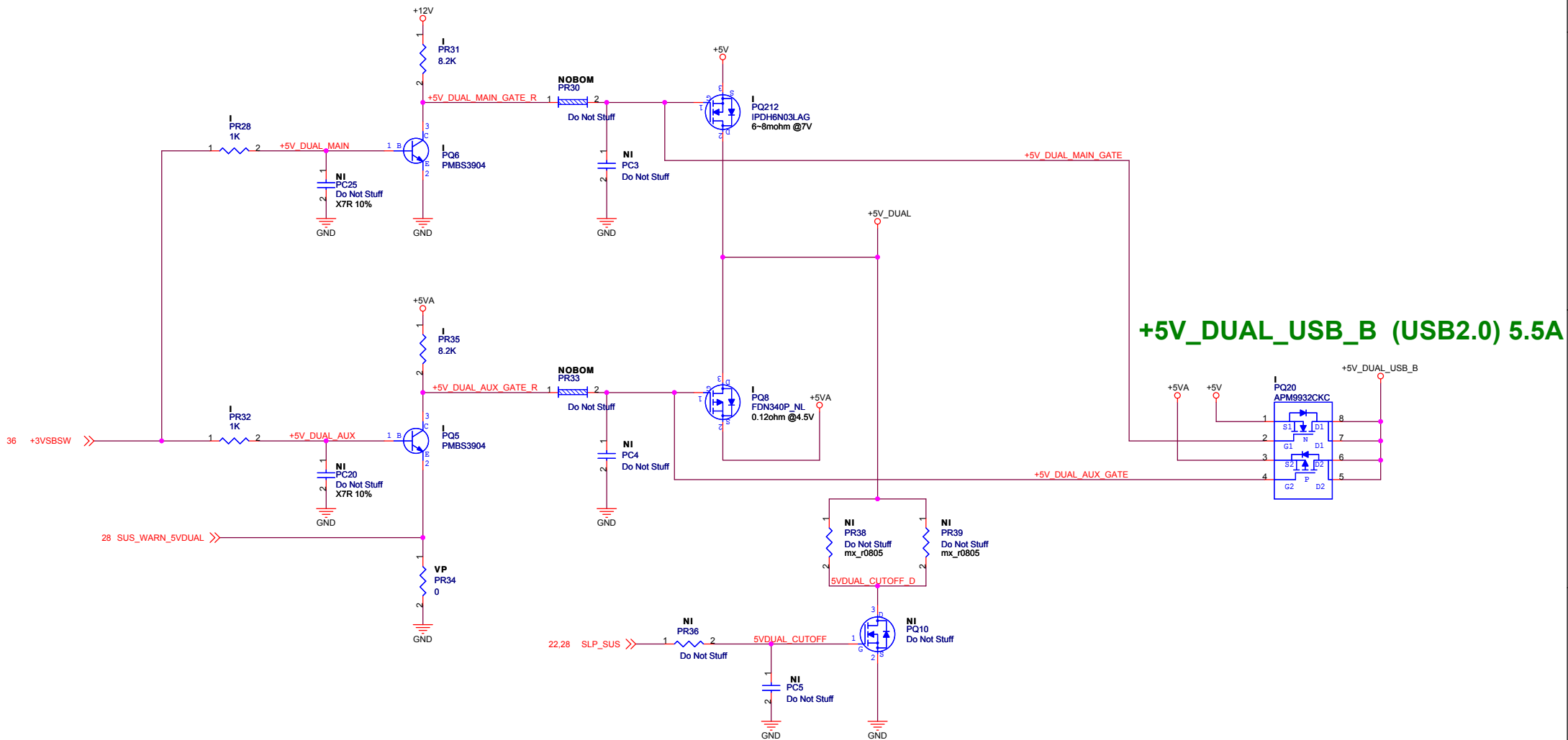
Size	Project Name	Rev
A3	IPISB-VR	1.01

Date: Wednesday, December 08, 2010 Sheet 61 of 76











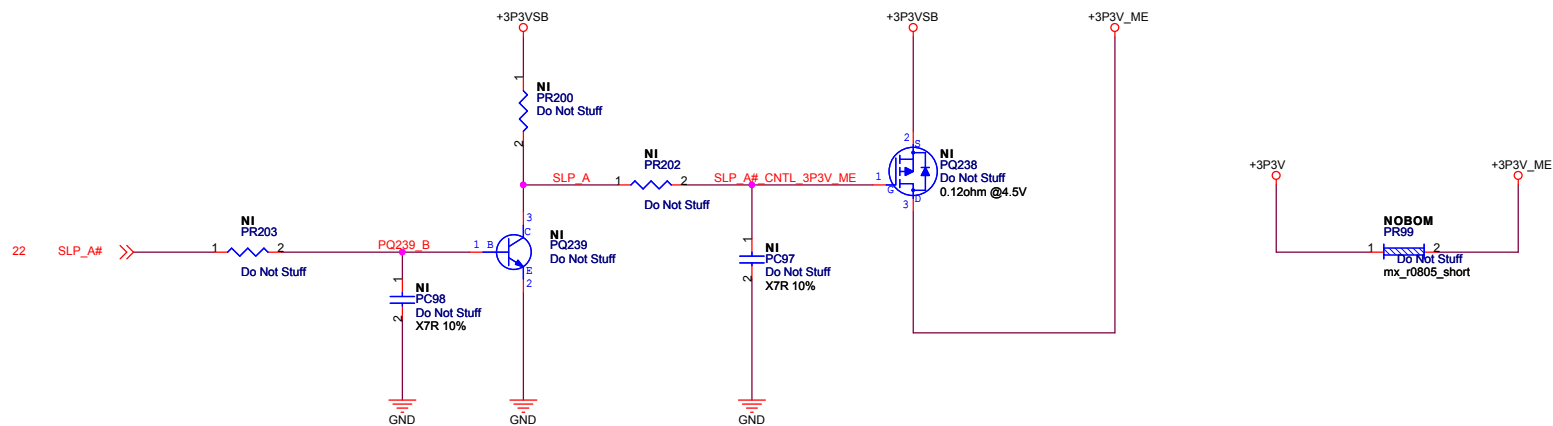
PEGATRON DT-MB RESTRICTED SECRET

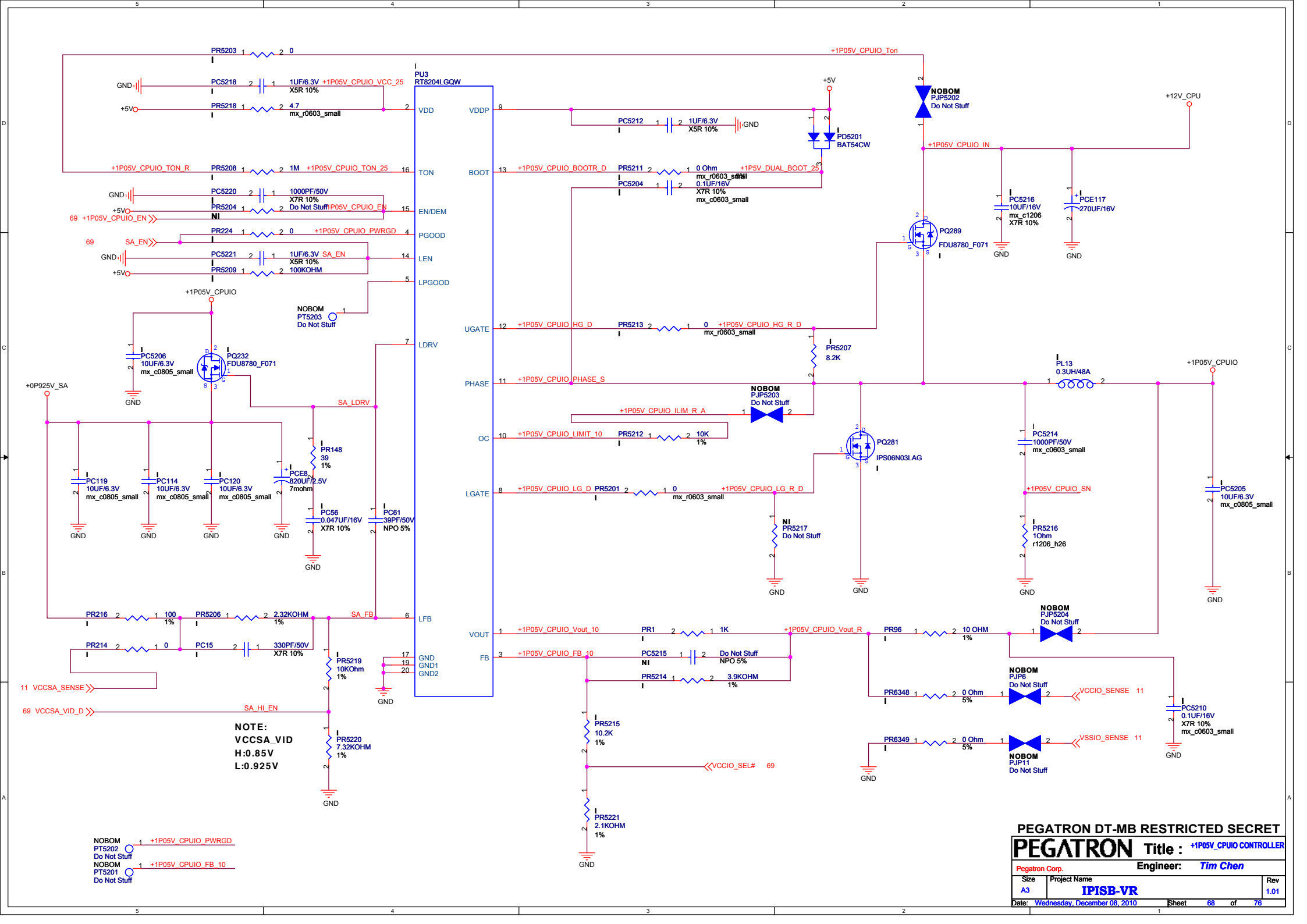
PEGATRON Title : **XXXX**

Pegatron Corp. Engineer: **Tim Chen**

Size	Project Name	Rev
A3	IPISB-VR	1.01

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NOTE:
VCCSA_VID
H:0.85V
L:0.925V

PEGATRON DT-MB RESTRICTED SECRET

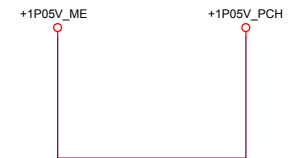
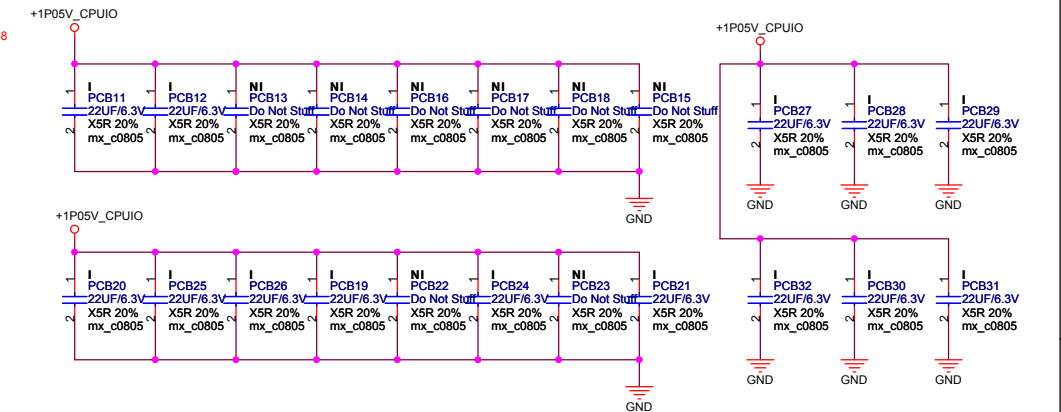
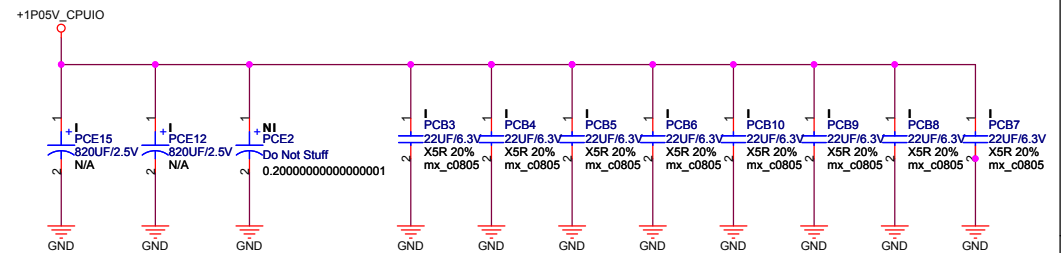
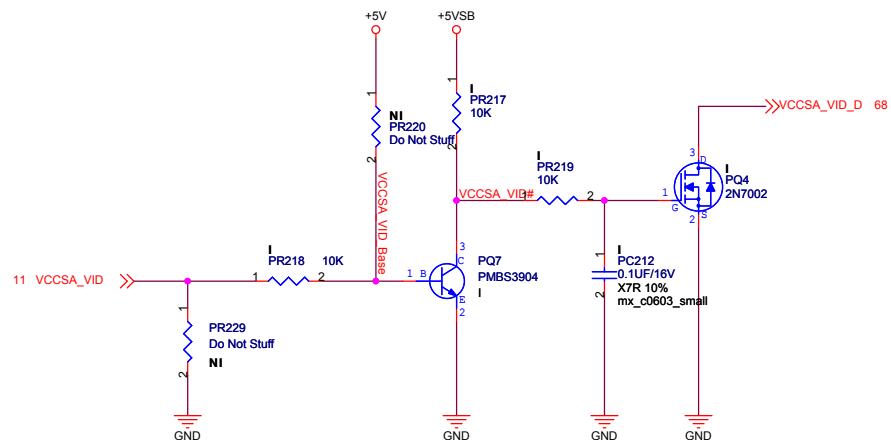
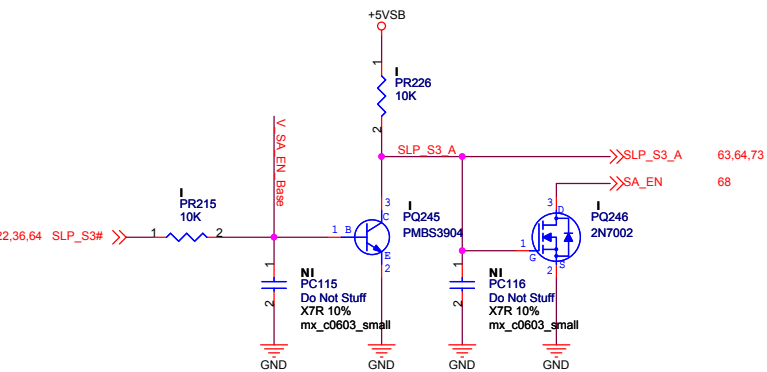
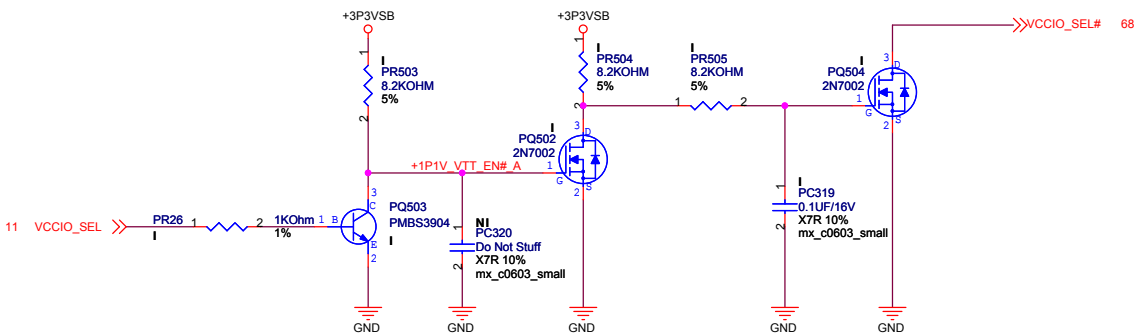
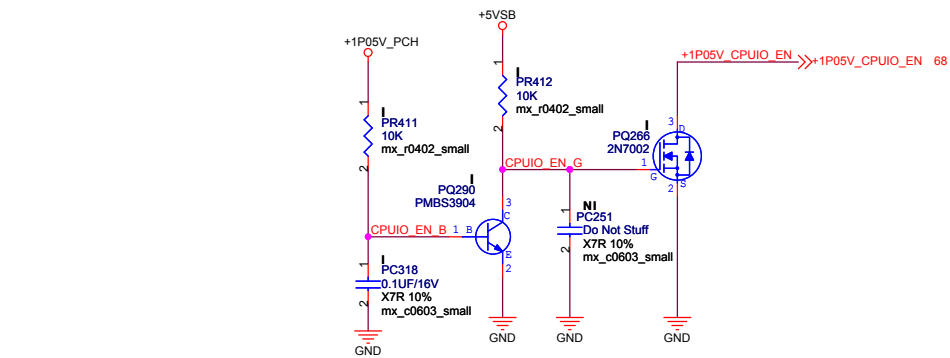
PEGATRON Title : +1P05V_CPUIO CONTROLLER

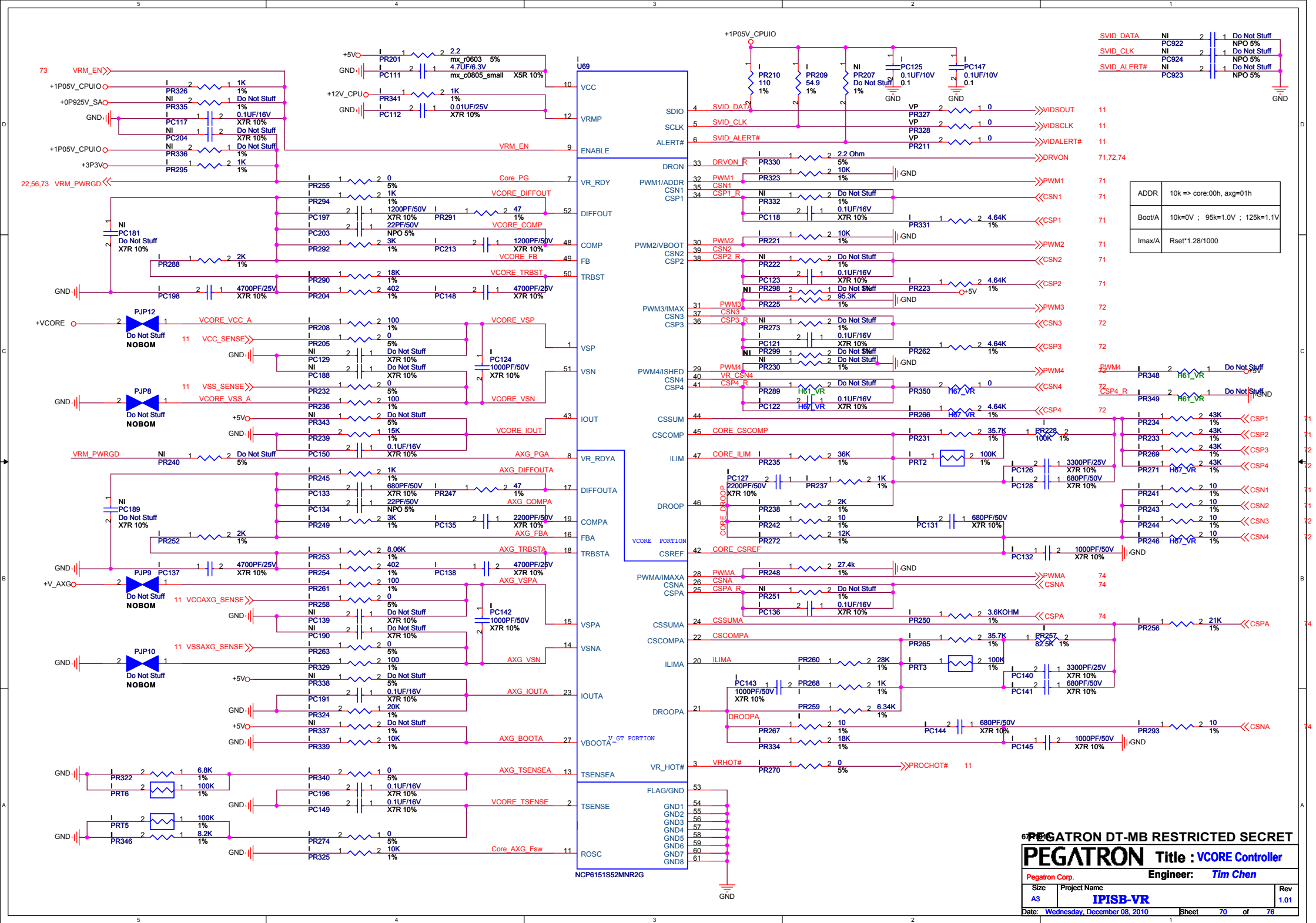
Pegatron Corp. Engineer: Tim Chen

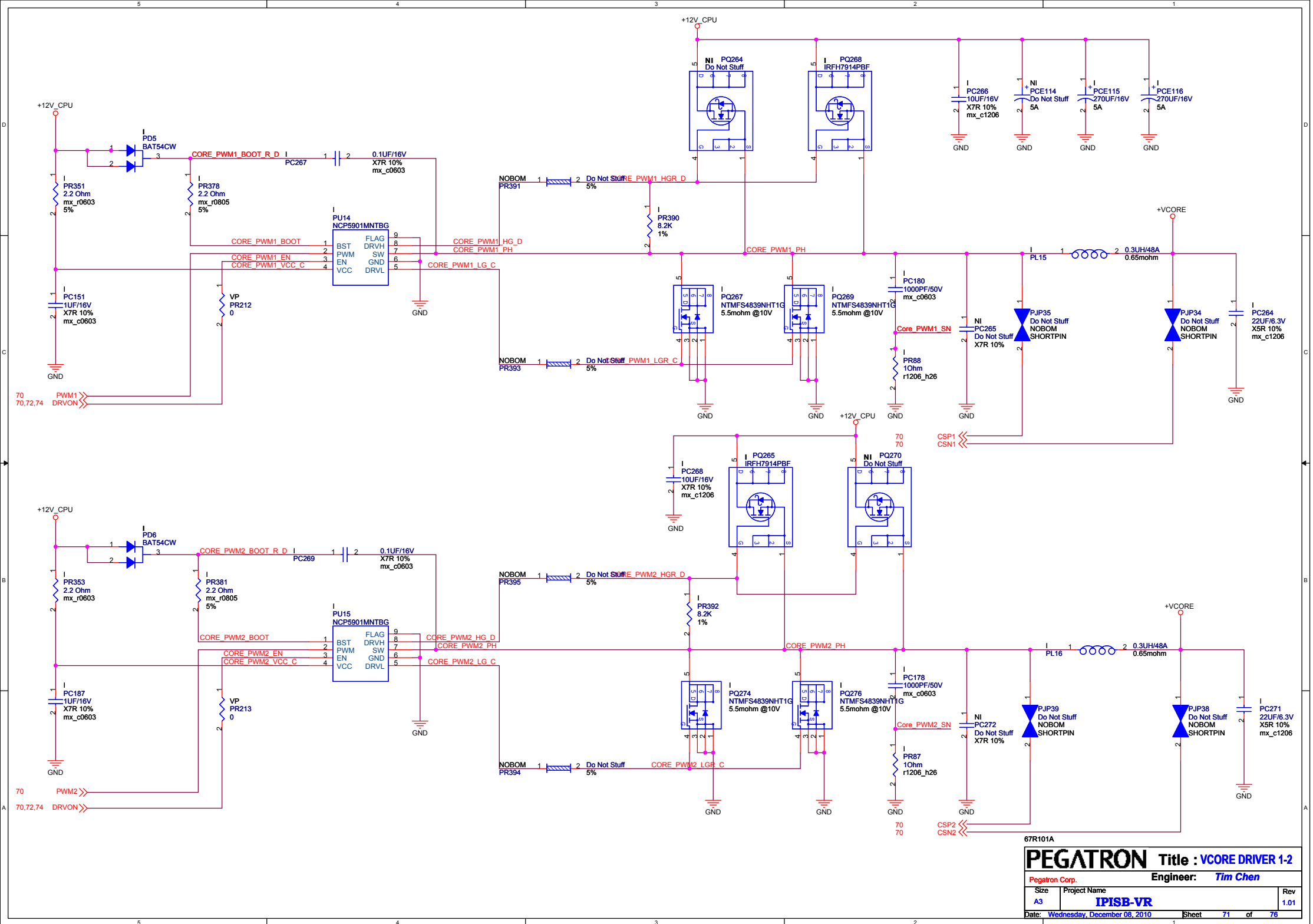
Size A3 Project Name IPISB-VR

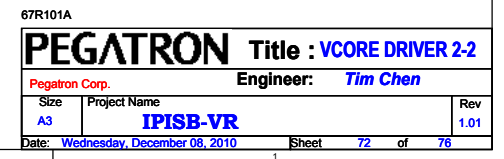
Date: Wednesday, December 08, 2010 Sheet 68 of 76

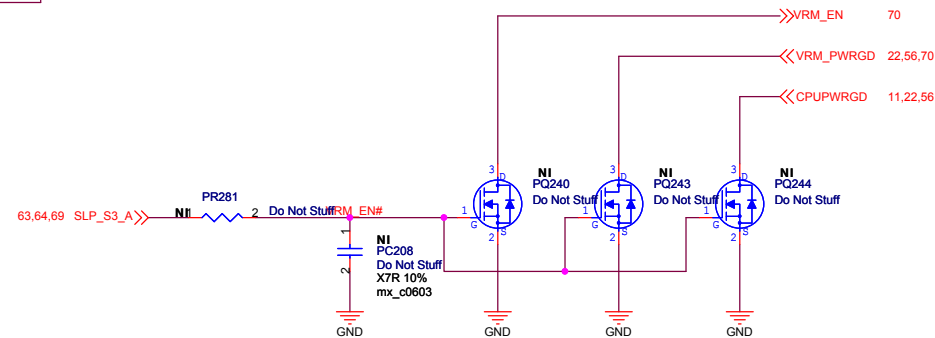
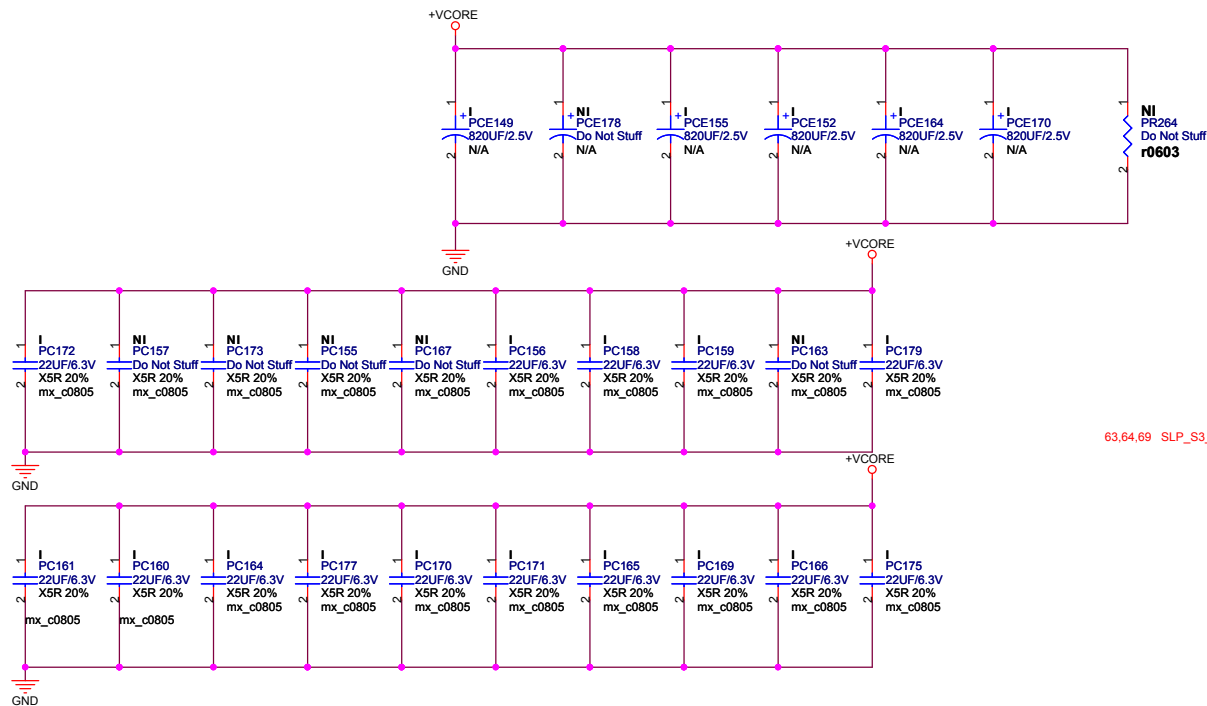
Rev 1.01

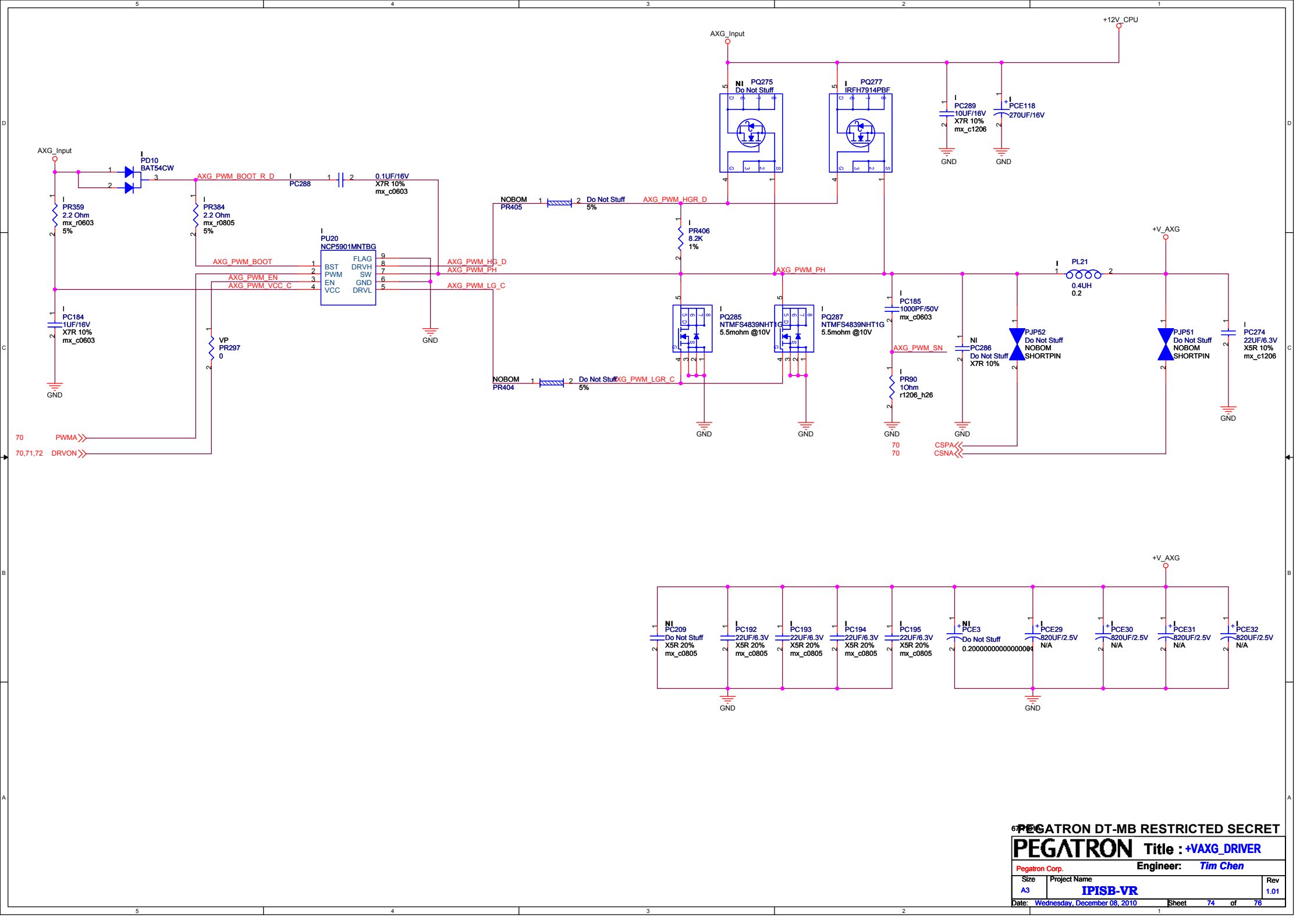














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PEGATRON DT-MB RESTRICTED SECRET

PEGATRON		Title : OV Controller	
PEGATRON CORP.		Engineer: Tim Chen	
Size A3	Project Name IPISB-VR	Rev 1.01	
Date: <u>Wednesday, December 08, 2010</u>		Sheet	75 of 76

